

UNCLASSIFIED

**IDENTIFICATION OF VOLATILE  
AND NON-VOLATILE STORAGE  
AND  
SANITIZATION OF SYSTEM  
COMPONENTS**

**JUNIPER NETWORKS  
SRX-SERIES  
SRX-MP-1SERIAL**

**REVISION 1.0  
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## 1 INTRODUCTION

### 1.1 Purpose

The purpose of this document is to provide direction to identify volatile and non-volatile storages and remove non-volatile (NV) storage from the Juniper Networks' SRX single-port synchronous serial mini-physical interface module(mini-PIM).

### 1.2 Scope

This document only provides direction for the identification and removal of NV storage components. It does not address destruction procedures for those components. As all of the NV storage components used in the SRX single-port synchronous serial mini-PIM are commercial off-the-shelf (COTS) components, directions for destruction of those components are left to the governing Department, Agency, or Office.

### 1.3 Description of Components

The non-volatile storage components on the SRX single-port synchronous serial mini-PIM include ID EEPROM, FPGA, CPLD and Renesas security chips, these components do not contain any user data. Users are not advised to remove these storage devices from the single-port synchronous serial mini-PIM unless they want to scrap the system. If the user wants to remove them, the user has to de-solder these chips.

The SRX single-port synchronous serial mini-PIM has the following non-volatile memory devices:

- Anti-counterfeit security chip's internal EEPROM – stores the signature of the security chip. No user data is stored on the chip.
- EEPROM – only used to store FRU identification data. No user data is stored on the chip.
- FPGA internal flash – stores FPGA image, no user data is stored on the chip.
- CPLD internal flash - stores CPLD image, no user data is stored on the chip.

The following pictures show the different views of the SRX-MP-1SERIAL:

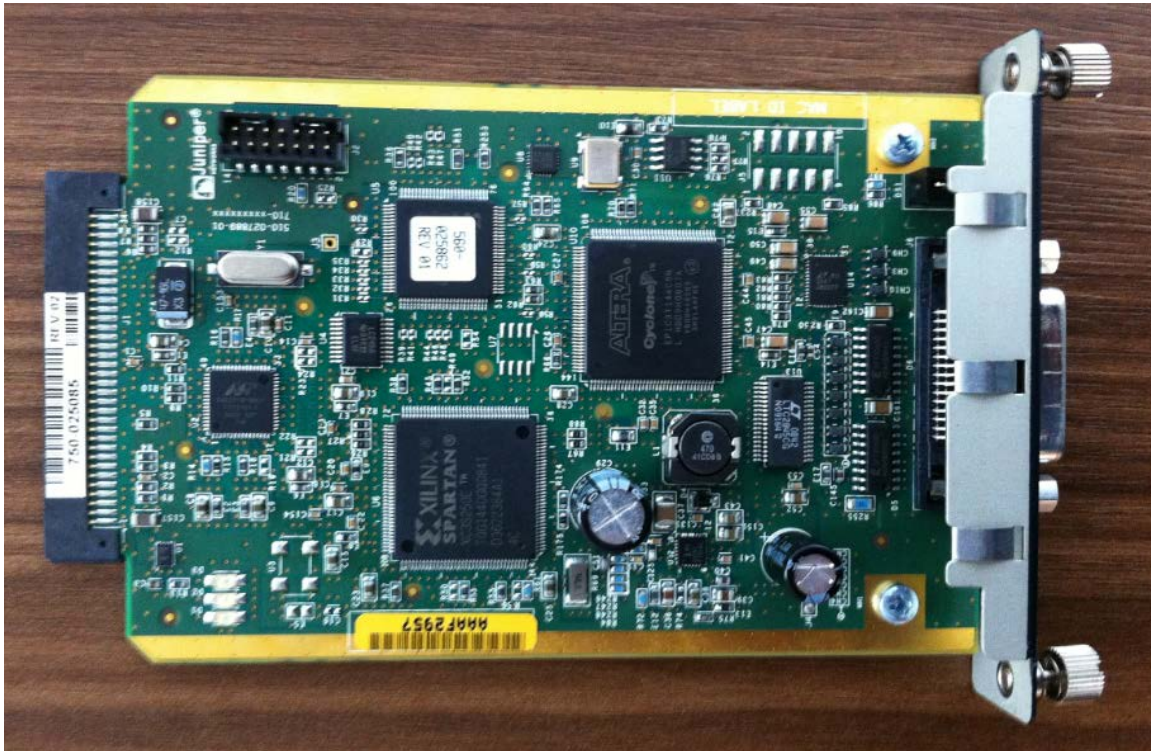


Figure 1-1: SRX-MP-1SERIAL different views