

**IDENTIFICATION OF VOLATILE
AND NON-VOLATILE STORAGE
AND
SANITIZATION OF SYSTEM
COMPONENTS**

**JUNIPER NETWORKS
EX3400-24P/24T/24T-DC**

**REVISION 1.0
June 09, 2016**

TABLE OF CONTENTS

1	Introduction	1
1.1	Purpose.....	1
1.2	Scope.....	1
2	Equipment Overview	1
2.1	Identification of Chassis	1
2.2	Description of Field Replaceable Units (FRU).....	2
3	Power Down and Removal of Non-Volatile Storage.....	2
3.1	System Power Down	2
3.2	Disassembly of the EX3400-24P/24T/24T-DC Chassis and Identification of NV storage.....	2
3.3	Removal of the NAND FLASH and SPI FLASH from the System Board .	6

TABLE OF FIGURES

Figure 2-1:	EX3400-24P/24T/24T-DC.....	2
Figure 3-1:	FAN FRU'S + PSU	3
Figure 3-2:	Top side screws	4
Figure 3-3:	Left side screws	4
Figure 3-4:	Right side screws	5
Figure 3-5:	Open Chassis.....	5
Figure 3-6:	Locate NV storage	6

1 INTRODUCTION

1.1 Purpose

The purpose of this document is to provide direction to identify and remove all non-volatile (NV) storage from the Juniper Networks EX3400-24P/24T/24T-DC platforms. Non-Volatile (NV) storage is a system memory that can store user data information and system configuration data even when system not powered. Volatile (V) storage is a system memory that only retains data or its contents while system powered but when system powered off or interrupted, its data or contents are immediately lost.

1.2 Scope

This document only addresses the EX3400-24P/24T/24T-DC platforms. While other platforms offered by Juniper Networks may contain similar hardware components, this document only applies to these devices. Furthermore, this document only provides direction for the identification and removal of NV storage components. It does not address destruction procedures for those components. As all of the NV storage components used in the EX3400-24P/24T/24T-DC are commercial off-the-shelf (COTS) components, directions for destruction of those components are left to the governing Department, Agency, or Office.

2 EQUIPMENT OVERVIEW

2.1 Identification of Chassis

EX3400 is mid-end 1G switch for high density wiring closet deployments. As a secondary use case, EX3400 can also be used as an entry level 1G ToR for Data Center deployments. The switches run under the JUNOS software, which provides Layer 2 and Layer 3 switching services. The same JUNOS code base that runs on EX3400-24P/24T/24T-DC switch also runs on all Juniper Networks products.

EX3400-24P/24T/24T-DC support 24 port 10/100/1000BASE-T Ethernet ports (access ports) with and without PoE, 4 SFP+ based 10Gb/s (10GbE) uplink ports and 2 QSFP based 40Gb/s Ports. EX3400-24P/24T/24T-DC is designed to fit in a standard 19" rack.



Figure 2-1: EX3400-24P/24T/24T-DC

2.2 Description of Field Replaceable Units (FRU)

The power supply, fan tray, and transceivers are hot-swappable. You can remove and replace them without powering off the system or disrupting system functions.

None of these components contain NV RAM. All NV RAM is either soldered or installed onto the system board.

3 POWER DOWN AND REMOVAL OF NON-VOLATILE STORAGE

In order to ensure that no user data or system configurations remain resident on a EX3400-24P/24T/24T-DC platform, the following steps must be performed:

1. Power must be removed from the system to clear all volatile storage
2. The NAND Flash modules must be removed from the system board
3. The SPI Flash components must be removed from the system board

A detailed process is included in the following sections.

3.1 System Power Down

Power down the system by removing any connected power cords from power supply.

3.2 Disassembly of the EX3400-24P/24T/24T-DC Chassis and Identification of NV storage

All NV RAM on EX3400-24P/24T/24T-DC are soldered to the system board. In order to access the memory for removal, refer to the following steps:

1. Remove the power supplies from the system. If there are no power supplies, remove the power supply Blanks. Fan FRU need not be removed. (Figure 3-1)
2. Remove the ear-mounts on both left and right side of the chassis if any.

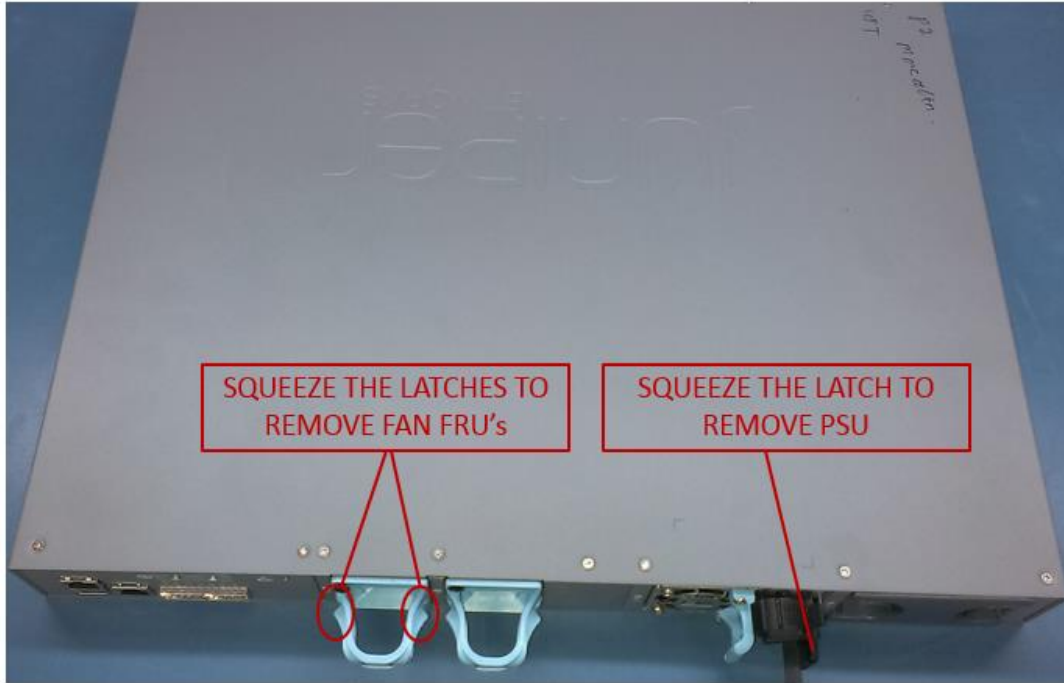


Figure 3-1: FAN FRU'S + PSU

3. Remove fourteen(14) screws from the top of the system (Figure 3-2)

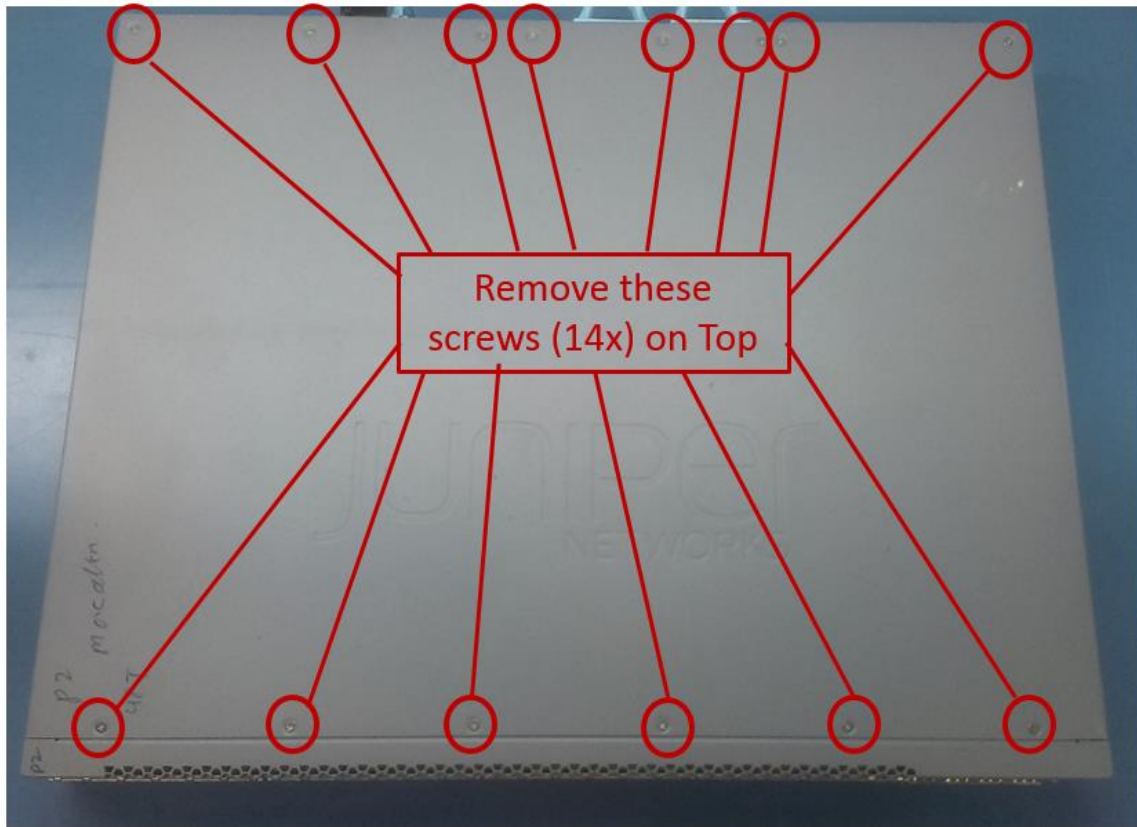


Figure 3-2: Top side screws

4. Remove six(6x) screws from left side of chassis (Figure 3-3)

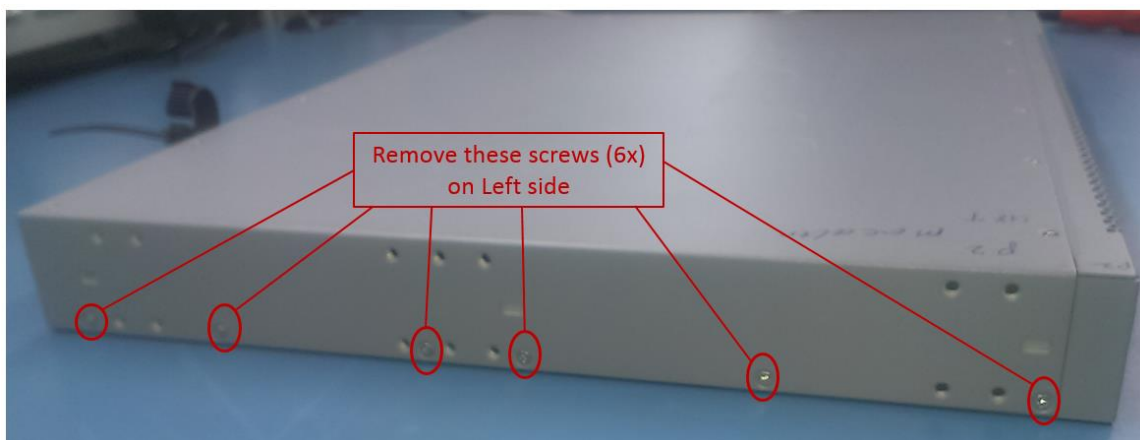


Figure 3-3: Left side screws

5. Remove six(6x) screws from right side of chassis (Figure 3-4)

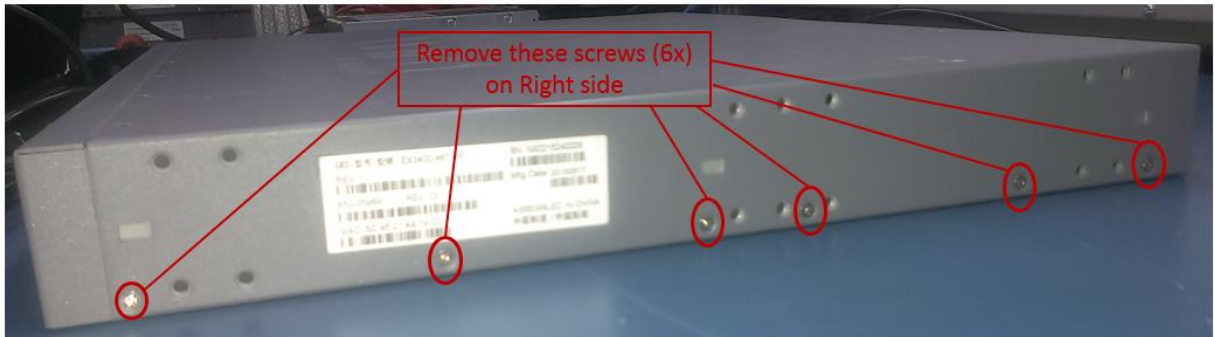


Figure 3-4: Right side screws

6. Remove the top of the chassis for Main board access (Figure 3-5)

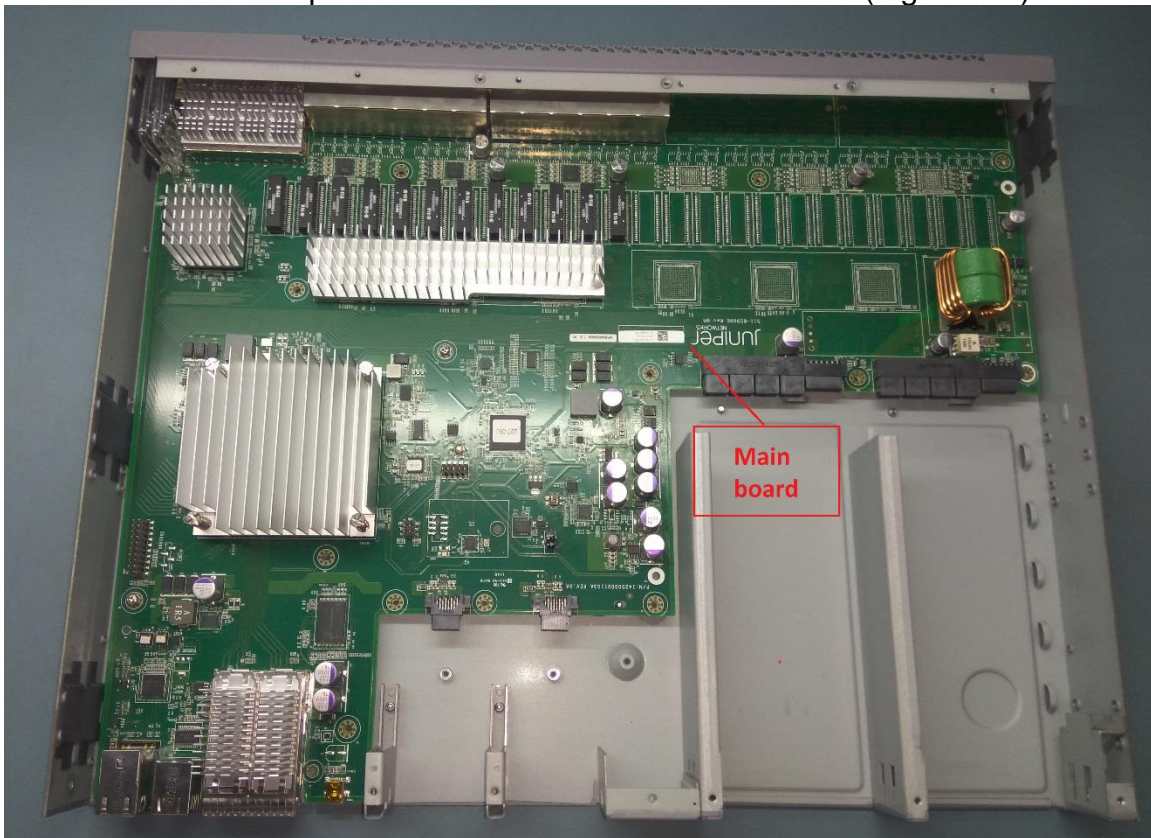


Figure 3-5: Open Chassis

7. Locate NV storage (Figure 3-6).

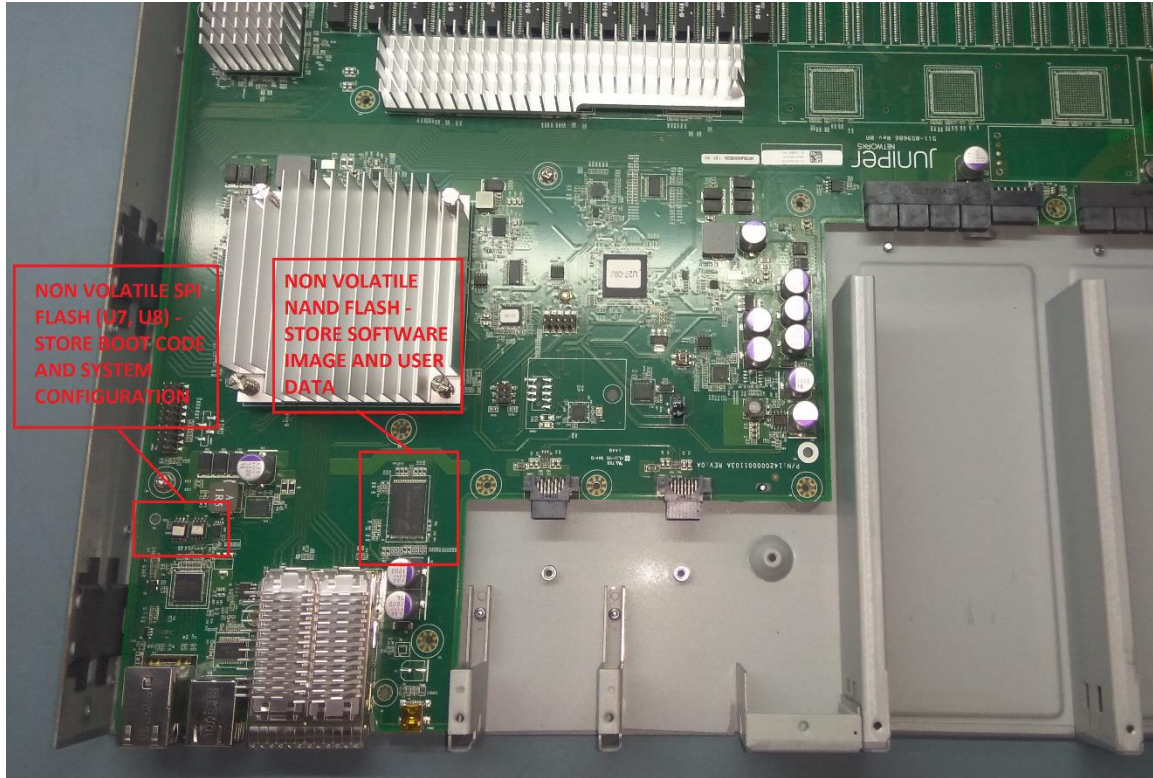


Figure 3-6: Locate NV storage

3.3 Removal of the NAND FLASH and SPI FLASH from the System Board

Once the NV storages has been located, remove them from the board.

NOTE : Before removal, ensure J-TAC and the appropriate account team has been notified of your intentions.