

# Letter of Volatility

## CTP2000 Series Products

The table below identifies all the memory locations in the CTP2000 series products. This includes the CTP2000 series processor, clock module, and the CTP2000 series T1E1 and Serial modules. All customer data (payload) is only stored in the FPGA, System RAM, Winpath chip set on the T1E1 module, packet memory, PCI controllers, and Ethernet controllers. These locations are transient and volatile. All the contents in these locations will be deleted when the power is turned off on the system. The term User Data in the table below includes node and network configuration data. That data is not customer [payload] data. The procedure for sanitization of User Data is identified in the table below. The Processor RTM (rear transition module) has no non-volatile storage on it.

CTP2000 Module	Part #	Device	Memory Size	Type	Volatility	User Data	Purpose	Sanitization Procedure
CTP2000-IM-8P	010098	FU2	328,844 bits FPGA Configuration FLASH + Logic Registers	Flash-Based PLD	Non-Volatile (Cfg Flash) and Volatile (Registers)	Yes	PLD for FPGA image management and Interface control	Power down system
CTP2000-IM-8P	010098	FU3	9,640,672 bits FPGA Configuration RAM + 1,369,728 bits FPGA User RAM + Logic Registers	SRAM-Based FPGA	Volatile	Yes	FPGA for data/packet/clock generation and routing	Power down system
CTP2000-IM-8P	010098	U29, U30	2M x 16 bits	DDR SDRAM	Volatile	No	Currently unused by system	N/A
CTP2000-IM-8P	010098	U56	32M x 1 bits	Serial Flash	Non-Volatile	No	FPGA image storage (2 images)	N/A
CTP2000-IM-8P	010098	U42	1K x 8 bits	Serial EEPROM	Non-Volatile	Yes	Storage for card serial numbers, calibration data, etc...	CTPOS PCB description edit (Node Test Menu)
CTP2000-IM-8P-T1E1	710-017723	U11	1K x 8 bits	Serial EEPROM	Non-Volatile	Yes	Storage for card serial numbers, calibration data, etc...	CTPOS PCB description edit (Node Test Menu)

CTP2000-IM-8P-T1E1	710-017723	U44	9,640,672 bits FPGA Configuration RAM + 1,369,728 bits FPGA User RAM + Logic Registers	SRAM- Based FPGA	Volatile	Yes	FPGA for data/packet/clock generation and routing	Power down system
CTP2000-IM-8P-T1E1	710-017723	U45,46,47 ,645,646,6 47	32M x 16 bits	DDR II SDRAM	Volatile	Yes	Winpath Network Processor code/data storage and packet buffers	Power down system
CTP2000-IM-8P-T1E1	710-017723	U74	4M x 16 bits	Flash ROM	Non-Volatile	No	Winpath Management MIPS processor runtime code storage	N/A
CTP2000-IM-8P-T1E1	710-017723	U70	64K x 8 bits	Serial EEPROM	Non-Volatile	No	Winpath Management MIPS processor boot code storage	N/A
CTP2000-IM-8P-T1E1	710-017723	U22	32M x 1 bits	Serial Flash	Non-Volatile	No	FPGA image storage (2 images)	N/A
CTP2000-IM-8P-T1E1	710-017723	U20	120,176 bits FPGA Configuration FLASH	Flash- Based PLD	Non-Volatile	No	PLD for FPGA image management	N/A
CTP-CLK-MAIN	710-026404	U11	120,176 bits FPGA Configuration FLASH	Flash- Based PLD	Non-Volatile	No	PLD for signal routing and control/status	N/A
CTP2000-PRC-S	PP 332/310	Main Memory	2048MB	DDR2 SDRAM	Volatile	No	System Memory	Power down system
	PP 332/310	CPU L1 Cache	32KB	SRAM	Volatile	No	Level one cache	Power down system
	PP 332/310	CPU L2 Cache	2MB	SRAM	Volatile	No	Level two cache	Power down system
	PP 332/310	Firmware memory	1MB	Flash PROM	Non-Volatile	No	Holds BIOS and Factory Test firmware	N/A
	PP 332/310	CMOS Memory	256 bytes	Battery backed SRAM	Non-Volatile when battery is fitted	No	Holds the time and date plus the BIOS configuration parameters	N/A
	PP 332/310	Graphics Memory	8MB	DDR2 SDRAM	Volatile	No	Video frame buffer	Power down system

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	PP 332/310	BIOS NV2 Memory	8KB	Serial EEPROM	Non-Volatile	No	Holds additional BIOS settings	N/A
	PP 332/310	Ethernet Controller Configuration EEPROM	8KB	Serial EEPROM	Non-Volatile	No	Holds MAC address and device configuration data	N/A
	PP 332/310	IPMI Microcontroller	64KB	Flash EPROM	Non-Volatile	no	Holds IPMI firmware	N/A
	PP 332/310	SDR/FRU EEPROM	8KB	Serial EEPROM	Non-Volatile	yes	Holds system inventory, configuration data and system events	Access the BIOS to delete this data