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Junos<sup>®</sup> OS

## Circuit Emulation Interfaces Feature Guide for Routing Devices



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# About the Documentation

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- Using the Examples in This Manual on page xiii
- Documentation Conventions on page xv
- Documentation Feedback on page xvii
- Requesting Technical Support on page xvii

## Documentation and Release Notes

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## Using the Examples in This Manual

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If you want to use the examples in this manual, you can use the **load merge** or the **load merge relative** command. These commands cause the software to merge the incoming configuration into the current candidate configuration. The example does not become active until you commit the candidate configuration.

If the example configuration contains the top level of the hierarchy (or multiple hierarchies), the example is a *full example*. In this case, use the **load merge** command.

If the example configuration does not start at the top level of the hierarchy, the example is a *snippet*. In this case, use the **load merge relative** command. These procedures are described in the following sections.

## Merging a Full Example

To merge a full example, follow these steps:

1. From the HTML or PDF version of the manual, copy a configuration example into a text file, save the file with a name, and copy the file to a directory on your routing platform.

For example, copy the following configuration to a file and name the file **ex-script.conf**. Copy the **ex-script.conf** file to the **/var/tmp** directory on your routing platform.

```
system {
  scripts {
    commit {
      file ex-script.xml;
    }
  }
}
interfaces {
  fxp0 {
    disable;
    unit 0 {
      family inet {
        address 10.0.0.1/24;
      }
    }
  }
}
```

2. Merge the contents of the file into your routing platform configuration by issuing the **load merge** configuration mode command:

```
[edit]
user@host# load merge /var/tmp/ex-script.conf
load complete
```

## Merging a Snippet

To merge a snippet, follow these steps:

1. From the HTML or PDF version of the manual, copy a configuration snippet into a text file, save the file with a name, and copy the file to a directory on your routing platform.

For example, copy the following snippet to a file and name the file **ex-script-snippet.conf**. Copy the **ex-script-snippet.conf** file to the **/var/tmp** directory on your routing platform.

```
commit {
  file ex-script-snippet.xml; }
```

2. Move to the hierarchy level that is relevant for this snippet by issuing the following configuration mode command:

```
[edit]
```

```
user@host# edit system scripts
[edit system scripts]
```

3. Merge the contents of the file into your routing platform configuration by issuing the **load merge relative** configuration mode command:

```
[edit system scripts]
user@host# load merge relative /var/tmp/ex-script-snippet.conf
load complete
```

For more information about the **load** command, see [CLI Explorer](#).

## Documentation Conventions

Table 1 on page xv defines notice icons used in this guide.

Table 1: Notice Icons







Icon	Meaning	Description
	Informational note	Indicates important features or instructions.
	Caution	Indicates a situation that might result in loss of data or hardware damage.
	Warning	Alerts you to the risk of personal injury or death.
	Laser warning	Alerts you to the risk of personal injury from a laser.
	Tip	Indicates helpful information.
	Best practice	Alerts you to a recommended use or implementation.

Table 2 on page xv defines the text and syntax conventions used in this guide.

Table 2: Text and Syntax Conventions

Convention	Description	Examples
<b>Bold text like this</b>	Represents text that you type.	To enter configuration mode, type the <b>configure</b> command:  user@host> <b>configure</b>

Table 2: Text and Syntax Conventions (continued)

Convention	Description	Examples
Fixed-width text like this	Represents output that appears on the terminal screen.	<pre>user@host&gt; show chassis alarms</pre> <p>No alarms currently active</p>
<i>Italic text like this</i>	<ul style="list-style-type: none"> <li>Introduces or emphasizes important new terms.</li> <li>Identifies guide names.</li> <li>Identifies RFC and Internet draft titles.</li> </ul>	<ul style="list-style-type: none"> <li>A policy <i>term</i> is a named structure that defines match conditions and actions.</li> <li><i>Junos OS CLI User Guide</i></li> <li>RFC 1997, <i>BGP Communities Attribute</i></li> </ul>
<i>Italic text like this</i>	Represents variables (options for which you substitute a value) in commands or configuration statements.	<p>Configure the machine's domain name:</p> <pre>[edit] root@# set system domain-name domain-name</pre>
Text like this	Represents names of configuration statements, commands, files, and directories; configuration hierarchy levels; or labels on routing platform components.	<ul style="list-style-type: none"> <li>To configure a stub area, include the <b>stub</b> statement at the <code>[edit protocols ospf area area-id]</code> hierarchy level.</li> <li>The console port is labeled <b>CONSOLE</b>.</li> </ul>
< > (angle brackets)	Encloses optional keywords or variables.	<pre>stub &lt;default-metric metric&gt;;</pre>
(pipe symbol)	Indicates a choice between the mutually exclusive keywords or variables on either side of the symbol. The set of choices is often enclosed in parentheses for clarity.	<pre>broadcast   multicast</pre> <p><i>(string1   string2   string3)</i></p>
# (pound sign)	Indicates a comment specified on the same line as the configuration statement to which it applies.	<pre>rsvp { # Required for dynamic MPLS only</pre>
[ ] (square brackets)	Encloses a variable for which you can substitute one or more values.	<pre>community name members [ community-ids ]</pre>
Indentation and braces ( { } )	Identifies a level in the configuration hierarchy.	<pre>[edit] routing-options {   static {     route default {       nexthop address;       retain;     }   } }</pre>
;(semicolon)	Identifies a leaf statement at a configuration hierarchy level.	
<b>GUI Conventions</b>		
<b>Bold text like this</b>	Represents graphical user interface (GUI) items you click or select.	<ul style="list-style-type: none"> <li>In the Logical Interfaces box, select <b>All Interfaces</b>.</li> <li>To cancel the configuration, click <b>Cancel</b>.</li> </ul>

Table 2: Text and Syntax Conventions (continued)

Convention	Description	Examples
> (bold right angle bracket)	Separates levels in a hierarchy of menu selections.	In the configuration editor hierarchy, select <b>Protocols&gt;Ospf</b> .

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- Find product documentation: <https://www.juniper.net/documentation/>
- Find solutions and answer questions using our Knowledge Base: <https://kb.juniper.net/>

- Download the latest versions of software and review release notes:  
<https://www.juniper.net/customers/csc/software/>
- Search technical bulletins for relevant hardware and software notifications:  
<https://kb.juniper.net/InfoCenter/>
- Join and participate in the Juniper Networks Community Forum:  
<https://www.juniper.net/company/communities/>
- Open a case online in the CSC Case Management tool: <https://www.juniper.net/cm/>

To verify service entitlement by product serial number, use our Serial Number Entitlement (SNE) Tool: <https://entitlementsearch.juniper.net/entitlementsearch/>

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- Use the Case Management tool in the CSC at <https://www.juniper.net/cm/>.
- Call 1-888-314-JTAC (1-888-314-5822 toll-free in the USA, Canada, and Mexico).

For international or direct-dial options in countries without toll-free numbers, see <https://www.juniper.net/support/requesting-support.html>.

## PART 1

# Overview

- [Understanding Circuit Emulation Interfaces on page 3](#)
- [Understanding How Circuit Emulation Interfaces Support Converged Networks That Accommodate Both IP And Legacy Services on page 13](#)



## CHAPTER 1

# Understanding Circuit Emulation Interfaces

- [Understanding Circuit Emulation Services and the Supported PIC Types on page 3](#)
- [Understanding Circuit Emulation PIC Clocking Features on page 8](#)
- [Understanding ATM QoS or Shaping on page 9](#)

## Understanding Circuit Emulation Services and the Supported PIC Types

Circuit emulation service is a method through which data can be transmitted over ATM, Ethernet, or MPLS networks. This information is error-free and has a constant delay, thereby enabling you to use it for services that use time-division multiplexing (TDM). This technology can be implemented through Structure-Agnostic TDM over Packet (SAToP) and Circuit Emulation Service over Packet-Switched Network (CESoPSN) protocols.

SAToP enables you to encapsulate TDM bit-streams such as T1, E1, T3, and E3 as pseudowires over packet-switched networks (PSNs).

CESoPSN enables you to encapsulate structured (NxDSO) TDM signals as pseudowires over packet-switching networks.

A pseudowire is a Layer 2 circuit or service, that emulates the essential attributes of a telecommunications service— such as a T1 line, over an MPLS PSN. The pseudowire is intended to provide only the minimum necessary functionality to emulate the wire with the required degree of faithfulness for the given service definition.

The following Circuit Emulation PICs are specifically designed for mobile backhaul applications.

- [4-Port Channelized OC3/STM1 \(Multi-Rate\) Circuit Emulation MIC with SFP on page 4](#)
- [12-Port Channelized T1/E1 Circuit Emulation PIC on page 5](#)
- [8-Port OC3/STM1 or 12-port OC12/STM4 ATM MIC on page 6](#)
- [16-Port Channelized E1/T1 Circuit Emulation MIC on page 6](#)
- [Layer 2 Circuit Standards on page 7](#)

## 4-Port Channelized OC3/STM1 (Multi-Rate) Circuit Emulation MIC with SFP

The 4-port Channelized OC3/STM1 (Multi-Rate) Circuit Emulation MIC with SFP—MIC-3D-4COC3-1COC12-CE—is a channelized Circuit Emulation MIC with rate-selectability. You can specify its port speed as **COC3-CSTM1** or **COC12-CSTM4**. The default port speed is **COC3-CSTM1**. To configure the 4-port Channelized OC3/STM1 Circuit Emulation MIC, see [“Configuring SAToP on 4-Port Channelized OC3/STM1 Circuit Emulation MICs” on page 19](#).

All ATM interfaces are either T1 or E1 channels within the COC3/CSTM1 hierarchy. Each COC3 interface can be partitioned as 3 COC1 slices, each of which in turn can be partitioned further into 28 ATM interfaces and the size of each interface created is that of a T1 interface. Each CS1 interface can be partitioned as 1 CAU4 interface, which can be further partitioned as E1-sized ATM interfaces.

The following features are supported on the MIC-3D-4COC3-1COC12-CE MIC:

- Per-MIC SONET/SDH framing
- Internal and loop clocking
- T1/E1 and SONET clocking
- Mixed SAToP and ATM interfaces on any port
- SONET mode—Each OC3 port can be channelized down to 3 COC1 channels, and then each COC1 can channel down to 28 T1 channels.
- SDH mode—Each STM1 port can be channelized down to 4 CAU4 channels, and then each CAU4 can channel down to 63 E1 channels.
- SAToP
- CESoPSN
- Pseudowire Emulation Edge to Edge (PWE3) control word for use over an MPLS PSN

The MIC-3D-4COC3-1COC12-CE MIC supports T1 and E1 options with the following exceptions:

- **bert-algorithm**, **bert-error-rate**, and **bert-period** options are supported for CT1 or CE1 configurations only.
- **framing** is supported for CT1 or CE1 configurations only. It is not applicable in SAToP configurations.
- **buildout** is supported in CT1 configurations only.
- **line-encoding** is supported in CT1 configurations only.
- **loopback local** and **loopback remote** are supported in CE1 and CT1 configurations only. By default, no loopback is configured.
- **loopback payload** is not supported. It is not applicable in SAToP configurations.
- **idle-cycle-flag** is not supported. It is not applicable in SAToP configurations.

- **start-end-flag** is not supported. It is not applicable in SAToP configurations.
- **invert-data** is not supported. It is not applicable in SAToP configurations.
- **fcs16** is not supported in E1 and T1 configurations only.
- **fcs32** is not supported in E1 and T1 configurations only. It is not applicable in SAToP configurations.
- **timeslots** is not supported. It is not applicable in SAToP or ATM configurations.
- **byte-encoding** is not supported in T1 configurations only. It is not applicable in SAToP configurations. **nx56** byte encoding is not supported.
- **crc-major-alarm-threshold** and **crc-minor-alarm-threshold** are T1 options supported in SAToP configurations only.
- **remote-loopback-respond** is not supported. It is not applicable in SAToP configurations.
- If you attempt to configure the local loopback capability on an *at-* interface—ATM1 or ATM2 intelligent queuing (IQ) interface or a virtual ATM interface on a Circuit Emulation (ce-) interface—by including the **loopback local** statement at the **[edit interfaces at-fpc/pic/port e1-options]**, **[edit interfaces at-fpc/pic/port e3-options]**, **[edit interfaces at-fpc/pic/port t1-options]**, or the **[edit interfaces at-fpc/pic/port t3-options]** hierarchy level (to define the E1, E3, T1, or T3 physical interface properties) and commit the configuration, the commit is successful. However, local loopback on AT interfaces does not take effect and a system log message is generated stating that local loopback is not supported. You must not configure local loopback because it is not supported on *at-* interfaces.
- Mixing T1 and E1 channels is not supported on individual ports.

For more information about MIC-3D-4COC3-1COC12-CE, see *Channelized OC3/STM1 (Multi-Rate) Circuit Emulation MIC with SFP*.

## 12-Port Channelized T1/E1 Circuit Emulation PIC

The 12-port Channelized T1/E1 Circuit Emulation PIC supports TDM interfaces by using the SAToP protocol [RFC 4553] encapsulation, and supports T1/E1 and SONET clocking features. The 12-port Channelized T1/E1 Circuit Emulation PIC can be configured to work as either 12 T1 interfaces or 12 E1 interfaces. Mixing T1 interfaces and E1 interfaces is not supported. To configure the 12-Port Channelized T1/E1 Circuit Emulation PIC, see [“Configuring the 12-Port Channelized T1/E1 Circuit Emulation PIC” on page 76](#).

The 12-port Channelized T1/E1 Circuit Emulation PICs support T1 and E1 options, with the following exceptions:

- **bert-algorithm**, **bert-error-rate**, and **bert-period** options are supported for CT1 or CE1 configurations only.
- **framing** is supported for CT1 or CE1 configurations only. It is not applicable in SAToP configurations.
- **buildout** is supported in CT1 configurations only.
- **line-encoding** is supported in CT1 configurations only.

- **loopback local** and **loopback remote** are supported in CE1 and CT1 configurations only.
- **loopback payload** is not supported. It is not applicable in SAToP configurations.
- **idle-cycle-flag** is not supported. It is not applicable in SAToP or ATM configurations.
- **start-end-flag** is not supported. It is not applicable in SAToP or ATM configurations.
- **invert-data** is not supported. It is not applicable in SAToP configurations.
- **fcs32** is not supported. **fcs** is not applicable in SAToP or ATM configurations.
- **timeslots** is not supported. It is not applicable in SAToP configurations.
- **byte-encoding nx56** is not supported. It is not applicable in SAToP or ATM configurations.
- **crc-major-alarm-threshold** and **crc-minor-alarm-threshold** are not supported.
- **remote-loopback-respond** is not supported. It is not applicable in SAToP configurations.

## 8-Port OC3/STM1 or 12-port OC12/STM4 ATM MIC

The 8-port OC3/STM1 or 2-port OC12/STM4 Circuit Emulation ATM MIC supports both SONET and SDH framing mode. The mode can be set at the MIC level or at the port level. ATM MICs are rate-selectable at the following rates: 2-port OC12 or 8-port OC3. The ATM MIC supports ATM pseudowire encapsulation and swapping of VPI and VCI values in both directions.



**NOTE:** Cell-relay VPI/VCI swapping and cell-relay VPI swapping on both egress and ingress are not compatible with the ATM policing feature.

## 16-Port Channelized E1/T1 Circuit Emulation MIC

The 16-port Channelized E1/T1 Circuit Emulation MIC (MIC-3D-16CHE1-T1-CE) is a channelized MIC with 16 E1 or T1 ports.

The following features are supported on the MIC-3D-16CHE1-T1-CE MIC:

- Each MIC can be separately configured in either T1 or E1 framing mode.
- Each T1 port supports superframe (D4) and extended superframe (ESF) framing modes.
- Each E1 port supports G704 with CRC4, G704 without CRC4, and unframed framing modes.
- Clear channel and  $N \times DS0$  channelization. For T1 the value of  $N$  ranges from 1 through 24 and for E1 the value of  $N$  ranges from 1 through 31.
- Diagnostic features:
  - T1/E1
  - T1 facilities data link (FDL)
  - Channel service unit (CSU)

- Bit error rate test (BERT)
- Juniper Integrity Test (JIT)
- T1/E1 alarm and performance monitoring (a Layer 1 OAM function)
- External (loop) timing and internal (system) timing
- TDM circuit emulation services CESoPSN and SAToP
- CoS parity with IQE PICs. The CoS features supported on MPCs are supported on this MIC.
- Encapsulations:
  - ATM CCC cell relay
  - ATM CCC VC multiplex
  - ATM VC multiplex
  - Multilink Point-to-Point Protocol (MLPPP)
  - Multilink Frame Relay (MLFR) FRF.15
  - Multilink Frame Relay (MLFR) FRF.16
  - Point-to-Point Protocol (PPP)
  - Cisco High-Level Data Link Control
- ATM class-of-service (CoS) features—traffic shaping, scheduling, and policing
- ATM Operation, Administration, and Maintenance
- Graceful Routing Engine switchover (GRES)

**NOTE:**

- When GRES is enabled you must execute the `clear interface statistics (interface-name | all)` operational mode command to reset the cumulative values for local statistics. For more information, see *Resetting Local Statistics*.
- Unified ISSU is not supported on the 16-port Channelized E1/T1 Circuit Emulation MIC (MIC-3D-16CHE1-T1-CE).

For more information about MIC-3D-16CHE1-T1-CE, see *Channelized E1/T1 Circuit Emulation MIC*.

## Layer 2 Circuit Standards

Junos OS substantially supports the following Layer 2 circuit standards:

- RFC 4447, *Pseudowire Setup and Maintenance Using the Label Distribution Protocol (LDP)* (except section 5.3)
- RFC 4448, *Encapsulation Methods for Transport of Ethernet over MPLS Networks*

- Internet draft draft-martini-l2circuit-encap-mpls-11.txt, *Encapsulation Methods for Transport of Layer 2 Frames Over IP and MPLS Networks* (expires August 2006)

Junos OS has the following exceptions:

- A packet with a sequence number of 0 is treated as out of sequence.
  - Any packet that does not have the next incremental sequence number is considered out of sequence.
  - When out-of-sequence packets arrive, the expected sequence number for the neighbor is set to the sequence number in the Layer 2 circuit control word.
- Internet draft draft-martini-l2circuit-trans-mpls-19.txt, *Transport of Layer 2 Frames Over MPLS* (expires September 2006).

These drafts are available on the IETF website at <http://www.ietf.org/>.

**Related  
Documentation**

- [Displaying Information About Circuit Emulation PICs on page 113](#)

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## Understanding Circuit Emulation PIC Clocking Features

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All Circuit Emulation PICs support the following clocking features:

- External clocking—Also known as *loop timing*. Clock is distributed via TDM interfaces.
- Internal clocking with external synchronization—Also known as *external timing* or *external synchronization*.
- Internal clocking with PIC-level line synchronization—The PIC's internal clock is synchronized with a clock recovered from a TDM interface local to the PIC.

This feature set is useful for aggregation in mobile backhaul applications.



**NOTE:** The primary reference source (PRS) of the clock recovered from one interface may not be the same as that of another TDM interface. There is a limitation on the number of timing domains that can be supported in practice.

**Related  
Documentation**

- [Understanding Mobile Backhaul on page 13](#)

## Understanding ATM QoS or Shaping

M7i, M10i, M40e, M120, and M320 routers with 4-port Channelized OC3/STM1 Circuit Emulation PICs and 12-port T1/E1 Circuit Emulation PICs and MX Series routers with Channelized OC3/STM1 (Multi-Rate) Circuit Emulation MIC with SFP and 16-port Channelized E1/T1 Circuit Emulation MIC support ATM pseudowire service with QoS features for ingress and egress direction traffic shaping. Policing is performed by monitoring the configured parameters on the incoming traffic and is also referred to as ingress shaping. Egress shaping uses queuing and scheduling to shape the outgoing traffic. Classification is provided per virtual circuit (VC). To configure ATM QoS or shaping, see [“Configuring ATM QoS or Shaping” on page 108](#).

The following QoS features are supported:

- CBR, rtVBR, nrtVBR, and UBR
- Policing on a per VC basis
- Independent PCR and SCR policing
- Counting policing actions

Circuit Emulation PICs provide pseudowire service towards the core. This section describes the ATM service QoS features.

Circuit Emulation PICs support two types of ATM pseudowires:

- cell—**atm-ccc-cell-relay** encapsulation
- aal5—**atm-ccc-vc-mux**



**NOTE:** Only ATM pseudowires are supported; no other encapsulation types are supported.

Since cells within a VC cannot be re-ordered, and since only the VC is mapped to a pseudowire, classification is not meaningful in the context of a pseudowire. However, different VCs can be mapped to different classes of traffic and can be classified in the core network.

Such a service would connect two ATM networks with an IP/MPLS core.

[Figure 1 on page 10](#) shows that the routers marked PE are equipped with Circuit Emulation PICs.

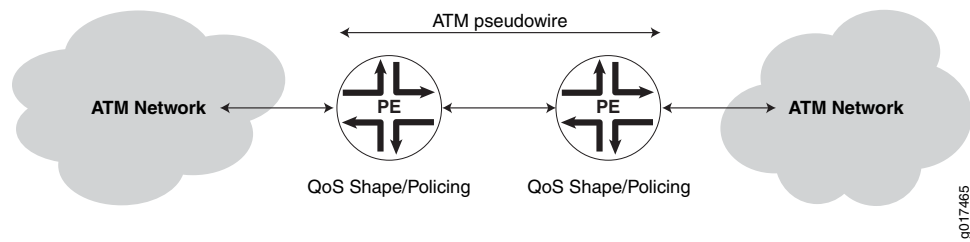
**Figure 1: Two ATM Networks with QoS Shaping and Pseudowire Connection**

Figure 1 on page 10 shows that traffic is shaped in the egress direction towards the ATM networks. In the ingress direction towards the core, the traffic is policed and the appropriate action is taken. Depending on a very elaborate state machine in the PIC, the traffic is either discarded or sent towards the core with a particular QoS class.

Each port has four transmit queues and one receive queue. Packets arrive from the ingress network on this single queue. Remember that this is per port and multiple VCs arrive on this queue, each with its own QoS class. To simplify unidirectional connections, only a Circuit Emulation PIC (PE 1 router) to Circuit Emulation PIC (PE 2 router) configuration is shown in Figure 2 on page 10.

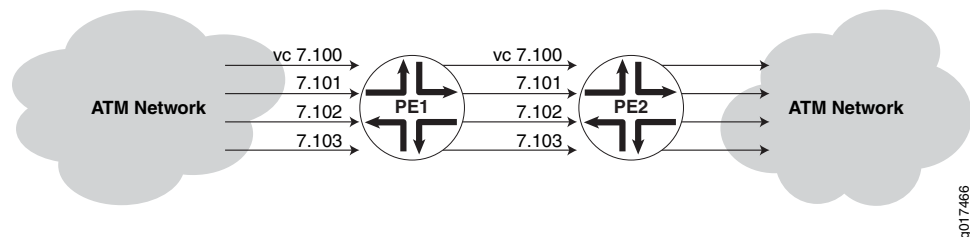
**Figure 2: VC Mapping with Circuit Emulation PICs**

Figure 2 on page 10 shows the four VCs with different classes mapped to different pseudowires in the core. Each VC has a different QoS class and is assigned a unique queue number. This queue number is copied to the EXP bits in the MPLS header as follows:

Qn concatenated with CLP -> EXP

Qn is 2 bits and can have four combinations; 00, 01, 10, and 11. Since CLP cannot be extracted from the PIC and put into each packet prefix, it is 0. The valid combinations are shown in Table 3 on page 10.

**Table 3: Valid EXP Bit Combinations**

Qn	CLP
00	0
01	0
10	0
11	0

For example, VC 7.100 has CBR, VC 7.101 has rt-VBR, 7.102 has nrt-VBR, 7.103 has UBR, and each VC is assigned a queue number as follows:

- VC 7.100 -> 00
- VC 7.101 -> 01
- VC 7.102 -> 10
- VC 7.103 -> 11



**NOTE:** Lower queue numbers have higher priorities.

---

Each VC will have the following EXP bits:

- VC 7.100 -> 000
- VC 7.101 -> 010
- VC 7.102 -> 100
- VC 7.103 -> 110

A packet arriving on VC 7.100 at the ingress router has the queue number 00 before being forwarded to the Packet Forwarding Engine. The Packet Forwarding Engine then translates this to 000 EXP bits in the core. At the egress router, the Packet Forwarding Engine retranslates this to queue 00 and stamps the packet with this queue number. The PIC receiving this queue number sends the packet out on the transmit queue that is mapped to queue 0, which could be the highest priority transmit queue on the egress side.

To briefly summarize, shaping and policing are possible. Classification is possible at the VC level by mapping a specific VC to a particular class.

**Related  
Documentation**

- [ATM Support on Circuit Emulation PICs Overview on page 71](#)
- [Configuring ATM QoS or Shaping on page 108](#)
- *shaping*



## CHAPTER 2

# Understanding How Circuit Emulation Interfaces Support Converged Networks That Accommodate Both IP And Legacy Services

- [Understanding Mobile Backhaul on page 13](#)

## Understanding Mobile Backhaul

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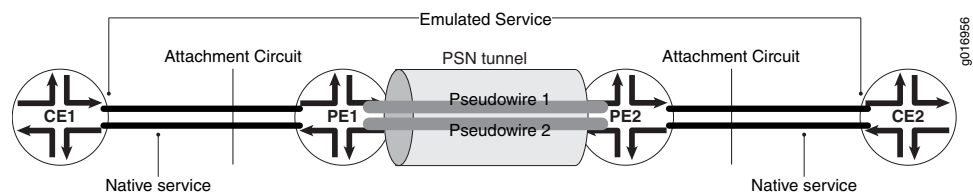
In a network of core routers, edge routers, access networks, and other components, the network paths that exist between the core network and edge subnetworks are known as backhaul. This backhaul can be designed as a wired backhaul setup or a wireless backhaul setup or as a combination of both on the basis of your requirement. In a mobile network, the network path between the cell tower and service provider is considered to be backhaul and is called mobile backhaul.

The following sections explain mobile backhaul application solution and IP/MPLS-based mobile backhaul solution.

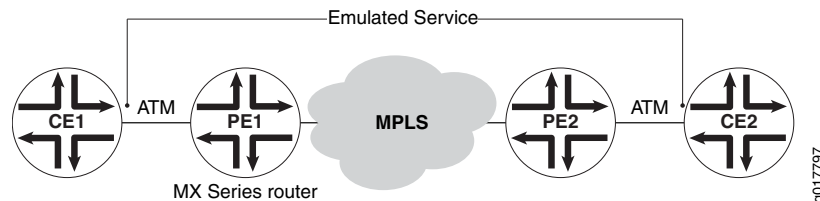
- [Mobile Backhaul Application Overview on page 13](#)
- [IP/MPLS-based Mobile Backhaul on page 14](#)

## Mobile Backhaul Application Overview

This topic provides an application example (see [Figure 3 on page 14](#)) based on the mobile backhaul reference model where customer edge 1 (CE1) is a base station controller (BSC), provider edge 1 (PE1) is a cell site router, PE2 is an M Series (aggregation) router, and CE2 is a BSC and Radio Network Controller (RNC). The Internet Engineering Task Force (RFC 3895) describes pseudowire as “a mechanism that emulates the essential attributes of a telecommunications service (such as a T1 leased line or Frame Relay) over a PSN” (Packet Switching Network).

**Figure 3: Mobile Backhaul Application**

For MX Series routers with ATM MICs with SFP, the mobile backhaul reference model is modified (see [Figure 4 on page 14](#)), where the provider edge 1 (PE1) router is an MX Series router with an ATM MIC with SFP. The PE2 router can be any router, such as an M Series (aggregation router) that might or might not support swapping (rewriting) of virtual path identifier (VPI) or virtual circuit identifier (VCI) values. An ATM pseudowire carries ATM cells over an MPLS network. The pseudowire encapsulation can be either cell relay or AAL5. Both modes enable sending of ATM cells between the ATM MIC and the Layer 2 network. You can configure the ATM MIC to swap the VPI value, VCI value, or both. You can also disable swapping of the values.

**Figure 4: Mobile Backhaul Application on MX Series Routers with ATM MICs with SFP**

## IP/MPLS-based Mobile Backhaul

Juniper Networks IP/MPLS-based mobile backhaul solutions provide the following benefits:

- Flexibility to support converged networks that accommodate both IP and legacy services (leveraging proven circuit emulation techniques).
- Scalability to support emerging data-intensive technologies.
- Cost-effectiveness to compensate for rising levels of backhaul traffic.

M7i, M10i, M40e, M120, and M320 routers with 12-port T1/E1 interfaces, 4-port Channelized OC3/STM1 interfaces, and MX Series routers with ATM MICs with SFP, with 2-port OC3/STM1 or 8-port OC12/STM4 circuit emulation interfaces, offer IP/MPLS-based mobile backhaul solutions that enable operators to combine diverse transport technologies onto a single transport architecture, to reduce operating costs while enhancing user features and increasing profits. This architecture accommodates the backhaul of legacy services, emerging IP-based services, location-based services, mobile gaming and mobile TV, and new emerging technologies such as LTE and WiMAX.

### Related Documentation

- [ATM Cell Relay Pseudowire VPI/VCI Swapping Overview on page 100](#)
- [no-vpivci-swapping on page 129](#)
- [psn-vci on page 130](#)

- [psn-vpi on page 131](#)



## PART 2

# Configuring Circuit Emulation Interfaces

- [Configuring SAToP Support on Circuit Emulation PICs on page 19](#)
- [Configuring SAToP Support on Circuit Emulation MICs on page 33](#)
- [Configuring CESoPSN Support on Circuit Emulation MIC on page 47](#)
- [Configuring ATM Support on Circuit Emulation PICs on page 71](#)



## CHAPTER 3

# Configuring SAToP Support on Circuit Emulation PICs

- [Configuring SAToP on 4-Port Channelized OC3/STM1 Circuit Emulation MICs on page 19](#)
- [Configuring SAToP Emulation on T1/E1 Interfaces on 12-Port Channelized T1/E1 Circuit Emulation PICs on page 26](#)
- [Setting the SAToP Options on page 30](#)

## Configuring SAToP on 4-Port Channelized OC3/STM1 Circuit Emulation MICs

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To configure Structure-Agnostic TDM over Packet (SAToP) on a 4-port Channelized OC3/STM1 Circuit Emulation MIC (MIC-3D-4COC3-1COC12-CE), you must configure the framing mode at the MIC level or port level and then configure each port as E1 interface or T1 interface.

- [Configuring SONET/SDH Rate-Selectability on page 19](#)
- [Configuring SONET/SDH Framing Mode at the MIC Level on page 20](#)
- [Configuring SONET/SDH Framing Mode at the Port Level on page 20](#)
- [Configuring SAToP Options on T1 interfaces on page 21](#)
- [Configuring SAToP Options on E1 Interfaces on page 23](#)

## Configuring SONET/SDH Rate-Selectability

You can configure rate-selectability on the Channelized OC3/STM1 (Multi-Rate) MICs with SFP by specifying its port speed as **COC3-CSTM1** or **COC12-CSTM4**.

To configure rate-selectability:

1. In configuration mode, go to the **[edit chassis fpc slot pic slot port slot]** hierarchy level.

```
[edit]
user@host# edit chassis fpc slot pic slot port slot
```

For example:

```
[edit]
user@host# edit chassis fpc 1 pic 0 port 0
```

2. Set the speed as **coc3-cstm1** or **coc12-cstm4**.

```
[edit chassis fpc slot pic slot port slot]
user@host# set speed (coc3-cstm1 | coc12-cstm4)
```

For example:

```
[edit chassis fpc 1 pic 0 port 0]
user@host# set speed coc3-cstm1
```



**NOTE:** When the speed is set as `coc12-cstm4`, instead of configuring COC3 ports down to T1 channels and CSTM1 ports down to E1 channels, you must configure COC12 ports down to T1 channels and CSTM4 channels down to E1 channels.

## Configuring SONET/SDH Framing Mode at the MIC Level

To configure framing mode at the MIC level:

1. Go to the `[edit chassis fpc fpc-slot pic pic-slot]` hierarchy level.

```
[edit]
[edit chassis fpc fpc-slot pic pic-slot]
```

2. Configure the framing mode as SONET for COC3 or SDH for CSTM1.

```
[edit chassis fpc fpc-slot pic pic-slot]
user@host# set framing (sonet | sdh)
```

After a MIC is brought online, interfaces are created for the MIC's available ports on the basis of the MIC type and the configured framing mode of each port:

- When the **framing sonet** statement (for a COC3 Circuit Emulation MIC) is enabled, four COC3 interfaces are created.
- When the **framing sdh** statement (for a CSTM1 Circuit Emulation MIC) is enabled, four CSTM1 interfaces are created.
- Note that when you do not specify framing mode at the MIC level, then the default framing mode is SONET for all the four ports.



**NOTE:** If you set the framing option incorrectly for the MIC type, the commit operation fails.

Bit error rate test (BERT) patterns with all ones received by T1/E1 interfaces on Circuit Emulation MICs configured for SAToP do not result in an alarm indication signal (AIS) defect. As a result, the T1/E1 interfaces remain up.

## Configuring SONET/SDH Framing Mode at the Port Level

Each port's framing mode can be configured individually, as either COC3 (SONET) or STM1 (SDH). Ports not configured for framing retain the MIC framing configuration, which

is SONET by default if you have not specified framing at the MIC level. To set the framing mode for individual ports, include the **framing** statement at the **[edit chassis fpc fpc-slot pic pic-slot port port-number]** hierarchy level:

To configure the framing mode as SONET for COC3 or SDH for CSTM1 at port level:

1. Go to the **[edit chassis fpc fpc-slot pic pic-slot port port-number]** hierarchy level.

```
[edit]
[edit chassis fpc fpc-slot pic pic-slot port port-number]
```

2. Configure the framing mode as SONET for COC3 or SDH for CSTM1.

```
[edit chassis fpc fpc-slot pic pic-slot port port-number]
user@host# set framing (sonet | sdh)
```



**NOTE:** Configuring the framing mode at the port level overwrites the previous MIC-level framing mode configuration for the specified port. Subsequently, configuring the MIC-level framing mode overwrites the port-level framing configuration. For example, if you want three STM1 ports and one COC3 port, then it is practical to first configure the MIC for SDH framing and then configure one port for SONET framing.

## Configuring SAToP Options on T1 interfaces

To configure the SAToP on an T1 interface, you must perform the following tasks:

1. [Configuring COC3 Ports Down to T1 Channels on page 21](#)
2. [Configuring SAToP Options on a T1 interface on page 22](#)

### Configuring COC3 Ports Down to T1 Channels

On any port (numbered 0 through 3) configured for SONET framing, you can configure three COC1 channels (numbered 1 through 3). On each COC1 channel, you can configure 28 T1 channels (numbered 1 through 28).

To configure COC3 channelization down to COC1 and then down to T1 channels:

1. In configuration mode, go to the **[edit interfaces coc3-fpc-slot/pic-slot/port]**

```
[edit]
user@host# edit interfaces coc3-fpc-slot/pic-slot/port
```

For example:

```
[edit]
user@host# edit interfaces coc3-1/0/0
```

2. Configure the sublevel interface partition index, range of SONET/SDH slices, and sublevel interface type.

```
[edit interfaces coc3-fpc-slot/pic-slot/port]
```

```
user@host# set partition partition-number oc-slice oc-slice interface-type coc1
```

For example:

```
[edit interfaces coc3-1/0/0]  
user@host# set partition 1 oc-slice 1 interface-type coc1
```

3. Enter **up** command to go to **[edit interfaces]** hierarchy level.

```
[edit interfaces coc3-fpc-slot/pic-slot/port]  
user@host# up
```

4. Configure the channelized OC1 interface, sublevel interface partition index, and the interface type.

```
[edit interfaces]  
user@host# set coc1-fpc-slot/pic-slot/port:channel-number partition partition-number  
interface-type t1
```

For example:

```
[edit interfaces]  
user@host# set coc1-1/0/0:1 partition 1 interface-type t1
```

5. Enter **up** to go to **[edit interfaces]** hierarchy level.
6. Configure the FPC slot, MIC slot and the port for T1 interface. Configure the encapsulation as SAToP and the logical interface for T1 interface.

```
[edit interfaces]  
user@host# set t1-fpc-slot/pic-slot/port:channel encapsulation encapsulation-type  
unit interface-unit-number;
```

For example:

```
[edit interfaces]  
user@host# set t1-1/0/:1 encapsulation satop unit 0;
```



**NOTE:** Similarly, you can configure the COC12 ports down to T1 channels. When configuring COC12 ports down to T1 channels, on a port configured for SONET framing, you can configure twelve COC1 channels (numbered 1 through 12). On each COC1 channel, you can configure 28 T1 channels (numbered 1 through 28).

---

After you partition the T1 channels, configure the SAToP options.

### Configuring SAToP Options on a T1 interface

To configure SAToP options on a T1 interface:

1. In configuration mode, go to the **[edit interfaces t1-*fpc-slot/pic-slot/port*]** hierarchy level.

```
[edit]
user@host# edit interfaces t1-fpc-slot/pic-slot/port
```

2. Use the **edit** command to go to the **satop-options** hierarchy level.

```
[edit interfaces t1-fpc-slot/pic-slot/port]
user@host# edit satop-options
```

3. Configure the following SAToP options:

- **excessive-packet-loss-rate**—Set packet loss options. The options are **sample-period** and **threshold**.

```
[edit interfaces t1-fpc-slot/pic-slot/port satop-options]
user@host# set excessive-packet-loss-rate sample-period sample-period threshold
percentile
```

- **idle-pattern**—An 8-bit hexadecimal pattern to replace TDM data in a lost packet (from 0 through 255).

```
[edit interfaces t1-fpc-slot/pic-slot/port satop-options]
user@host# set idle-pattern pattern
```

- **jitter-buffer-auto-adjust**—Automatically adjust the jitter buffer.

```
[edit interfaces t1-fpc-slot/pic-slot/port satop-options]
user@host# set jitter-buffer-auto-adjust
```



**NOTE:** The jitter-buffer-auto-adjust option is not applicable on MX Series routers.

- **jitter-buffer-latency**—Time delay in the jitter buffer (from 1 through 1000 milliseconds).

```
[edit interfaces t1-fpc-slot/pic-slot/port satop-options]
user@host# set jitter-buffer-latency milliseconds
```

- **jitter-buffer-packets**—Number of packets in the jitter buffer (from 1 through 64 packets).

```
[edit interfaces t1-fpc-slot/pic-slot/port satop-options]
user@host# set jitter-buffer-packets packets
```

- **payload-size**—Configure the payload size, in bytes (from 32 through 1024 bytes).

```
[edit interfaces t1-fpc-slot/pic-slot/port satop-options]
user@host# set payload-size bytes
```

## Configuring SAToP Options on E1 Interfaces

To configure the SAToP on an E1 interface.

1. [Configuring CSTMI Ports Down to E1 Channels on page 24](#)
2. [Configuring SAToP Options on E1 Interfaces on page 25](#)

### Configuring CSTM1 Ports Down to E1 Channels

---

On any port (numbered 0 through 3) configured for SDH framing, you can configure one CAU4 channel. On each CAU4 channel, you can configure 63 E1 channels (numbered 1 through 63).

To configure CSTM1 channelization down to CAU4 and then down to E1 channels.

1. In configuration mode, go to the **[edit interfaces cstm1-fpc-slot/pic-slot/port]**

```
[edit]
[edit interfaces cstm1-fpc-slot/pic-slot/port]
```

For example:

```
[edit]
[edit interfaces cstm1-1/0/1]
```

2. Configure the channelize interface as clear channel and the set the interface-type as cau4

```
[edit interfaces cstm1-fpc-slot/pic-slot/port]
user@host# set no-partition interface-type cau4;
```

3. Enter **up** to go to **[edit interfaces]** hierarchy level.

4. Configure the FPC slot, MIC slot and the port for CAU4 interface. Configure the sublevel interface partition index and the interface type as E1.

```
[edit interfaces]
user@host# set cau4-fpc-slot/pic-slot/port partition partition-number interface-type
e1
```

For example:

```
[edit interfaces]
user@host# set cau4-1/0/1 partition 1 interface-type e1
```

5. Enter **up** to go to **[edit interfaces]** hierarchy level.

6. Configure the FPC slot, MIC slot and the port for E1 interface. Configure the encapsulation as SAToP and the logical interface for E1 interface.

```
[edit interfaces]
user@host# set e1-fpc-slot/pic-slot/port:channel encapsulation encapsulation-type
unit interface-unit-number;
```

For example:

```
[edit interfaces]
user@host# set e1-1/0/:1 encapsulation satop unit 0;
```



**NOTE:** Similarly, you can configure the CSTM4 channels down to E1 channels.

After you configure the E1 channels, configure the SAToP options.

### Configuring SAToP Options on E1 Interfaces

To configure SAToP options on E1 interfaces:

1. In configuration mode, go to the **[edit interfaces e1-fpc-slot/pic-slot/port]** hierarchy level.

```
[edit]
user@host# edit interfaces e1-fpc-slot/pic-slot/port
```

2. Use the **edit** command to go to the **satop-options** hierarchy level.

```
[edit interfaces e1-fpc-slot/pic-slot/port]
user@host# edit satop-options
```

3. Configure the following SAToP options:

- **excessive-packet-loss-rate**—Set packet loss options. The options are **sample-period** and **threshold**.

```
[edit interfaces e1-fpc-slot/pic-slot/port satop-options]
user@host# set excessive-packet-loss-rate sample-period sample-period threshold
percentile
```

- **idle-pattern**—An 8-bit hexadecimal pattern to replace TDM data in a lost packet (from 0 through 255).

```
[edit interfaces e1-fpc-slot/pic-slot/port satop-options]
user@host# set idle-pattern pattern
```

- **jitter-buffer-auto-adjust**—Automatically adjust the jitter buffer.

```
[edit interfaces e1-fpc-slot/pic-slot/port satop-options]
user@host# set jitter-buffer-auto-adjust
```



**NOTE:** The jitter-buffer-auto-adjust option is not applicable on MX Series routers.

- **jitter-buffer-latency**—Time delay in the jitter buffer (from 1 through 1000 milliseconds).

```
[edit interfaces e1-fpc-slot/pic-slot/port satop-options]
user@host# set jitter-buffer-latency milliseconds
```

- **jitter-buffer-packets**—Number of packets in the jitter buffer (from 1 through 64 packets).

```
[edit interfaces e1-fpc-slot/pic-slot/port satop-options]
user@host# set jitter-buffer-packets packets
```

- **payload-size**—Configure the payload size, in bytes (from 32 through 1024 bytes).

```
[edit interfaces e1-fpc-slot/pic-slot/port satop-options]  
user@host# set payload-size bytes
```

**Related  
Documentation**

- [Understanding Circuit Emulation Services and the Supported PIC Types on page 3](#)

---

## Configuring SAToP Emulation on T1/E1 Interfaces on 12-Port Channelized T1/E1 Circuit Emulation PICs

---

The following sections describes configuring SAToP on the 12-port Channelized T1/E1 Circuit Emulation PICs:

- [Setting the Emulation Mode on page 26](#)
- [Configuring SAToP Emulation on T1/E1 Interfaces on page 26](#)

### Setting the Emulation Mode

To set the framing emulation mode, include the **framing** statement at the **[edit chassis fpc fpc-slot pic pic-slot]** hierarchy level:

```
[edit chassis fpc fpc-slot pic pic-slot]  
user@host# set framing (t1 | e1);
```

After a PIC is brought online, interfaces are created for the PIC's available ports according to the PIC type and the framing option used:

- If you include the **framing t1** statement (for a T1 Circuit Emulation PIC), 12 CT1 interfaces are created.
- If you include the **framing e1** statement (for an E1 Circuit Emulation PIC), 12 CE1 interfaces are created.



**NOTE:** If you set the framing option incorrectly for the PIC type, the commit operation fails.

Circuit Emulation PICs with SONET and SDH ports require prior channelization down to T1 or E1 before you can configure them. Only T1/E1 channels support SAToP encapsulation or SAToP options.

Bit error rate test (BERT) patterns with all ones received by T1/E1 interfaces on Circuit Emulation PICs configured for SAToP do not result in an alarm indication signal (AIS) defect. As a result, the T1/E1 interfaces remain up.

---

### Configuring SAToP Emulation on T1/E1 Interfaces

- [Setting the Encapsulation Mode on page 27](#)
- [Configuring Loopback for a T1 Interface or an E1 Interface on page 27](#)

- [Setting the SAToP Options on page 27](#)
- [Configuring the Pseudowire Interface on page 29](#)

### Setting the Encapsulation Mode

E1 channels on Circuit Emulation PICs can be configured with SAToP encapsulation at the provider edge (PE) router, as follows:



**NOTE:** The below mentioned procedure can be used to configure T1 channels on circuit emulation PICs with SAToP encapsulation at the PE router.

1. In the configuration mode, go to **[edit interfaces e1-fpc-slot/pic-slot/port]** hierarchy level.

```
[edit]
user@host# [edit interfaces e1 fpc-slot/pic-slot/port]
```

For example:

```
[edit]
[edit interfaces e1-1/0/0]
```

2. Configure SAToP encapsulation and the logical interface for E1 interface

```
[edit interfaces e1-1/0/0]
user@host# set encapsulation encapsulation-type unit interface-unit-number;
```

For example:

```
[edit interfaces e1-1/0/0]
user@host# set encapsulation satop unit 0;
```

You do not need to configure any cross-connect circuit family because it is automatically created for the above encapsulation.

### Configuring Loopback for a T1 Interface or an E1 Interface

To configure loopback capability between the local T1 interface and the remote channel service unit (CSU), see *Configuring T1 Loopback Capability*. To configure loopback capability between the local E1 interface and the remote channel service unit (CSU), see *Configuring E1 Loopback Capability*.



**NOTE:** By default, no loopback is configured.

### Setting the SAToP Options

To configure SAToP options on T1/E1 interfaces:

1. In configuration mode, go to the **[edit interfaces e1-fpc-slot/pic-slot/port]** hierarchy level.

```
[edit]
```

```
user@host# edit interfaces e1-fpc-slot/pic-slot/port
```

For example:

```
[edit]
user@host# edit interfaces e1-1/0/0
```

2. Use the **edit** command to go to the **satop-options** hierarchy level.

```
[edit]
user@host# edit satop-options
```

3. In this hierarchy level, using the **set** command you can configure the following SAToP options:

- **excessive-packet-loss-rate**—Set packet loss options. The options are **groups**, **sample-period**, and **threshold**.
  - **groups**—Specify groups.
  - **sample-period**—Time required to calculate excessive packet loss rate (from 1000 through 65,535 milliseconds).
  - **threshold**—Percentile designating the threshold of excessive packet loss rate (1–100 percent).
- **idle-pattern**—An 8-bit hexadecimal pattern to replace TDM data in a lost packet (from 0 through 255).
- **jitter-buffer-auto-adjust**—Automatically adjust the jitter buffer.



**NOTE:** The **jitter-buffer-auto-adjust** option is not applicable on MX Series routers.

- **jitter-buffer-latency**—Time delay in the jitter buffer (from 1 through 1000 milliseconds).
- **jitter-buffer-packets**—Number of packets in the jitter buffer (from 1 through 64 packets).
- **payload-size**—Configure the payload size, in bytes (from 32 through 1024 bytes).



**NOTE:** In this section, we are configuring only one SAToP option. You can follow the same method to configure all the other SAToP options.

```
[edit interfaces e1-1/0/0 satop-options]
user@host# set excessive-packet-loss-rate sample-period sample-period
```

For example:

```
[edit interfaces e1-1/0/0 satop-options]
user@host# set excessive-packet-loss-rate sample-period 4000
```

To verify this configuration, use the **show** command at the **[edit interfaces e1-1/0/0]** hierarchy level:

```
[edit interfaces e1-1/0/0]
user@host# show
satop-options {
  excessive-packet-loss-rate {
    sample-period 4000;
  }
}
```

See Also • [satop-options on page 132](#)

### Configuring the Pseudowire Interface

To configure the TDM pseudowire at the provider edge (PE) router, use the existing Layer 2 circuit infrastructure, as shown in the following procedure:

1. In the configuration mode, go to **[edit protocols l2circuit]** hierarchy level.

```
[edit]
user@host# edit protocol l2circuit
```

2. Configure the IP address of the neighboring router or switch, interface forming the layer 2 circuit and the identifier for the layer 2 circuit.

```
[edit protocol l2circuit]
user@host# set neighbor ip-address interface
interface-name-fpc-slot/pic-slot/port.interface-unit-number virtual-circuit-id
virtual-circuit-id;
```



**NOTE:** To configure T1 interface as the layer 2 circuit, replace e1 with t1 in the below statement.

For example:

```
[edit protocol l2circuit]
user@host# set neighbor 10.255.0.6 interface e1-1/0/0.0 virtual-circuit-id 1
```

3. To verify the configuration use the **show** command at the **[edit protocols l2circuit]** hierarchy level.

```
[edit protocols l2circuit]
user@host# show
neighbor 10.255.0.6 {
  interface e1-1/0/0.0 {
    virtual-circuit-id 1;
  }
}
```

After the customer edge (CE)-bound interfaces (for both PE routers) are configured with proper encapsulation, payload size, and other parameters, the two PE routers try to

establish a pseudowire with Pseudowire Emulation Edge-to-Edge (PWE3) signaling extensions. The following pseudowire interface configurations are disabled or ignored for TDM pseudowires:

- **ignore-encapsulation**
- **mtu**

The supported pseudowire types are:

- 0x0011 Structure-Agnostic E1 over Packet
- 0x0012 Structure-Agnostic T1 (DS1) over Packet

When the local interface parameters match the received parameters, and the pseudowire type and control word bit are equal, the pseudowire is established.

For detailed information about configuring TDM pseudowire, see the *Junos OS VPNs Library for Routing Devices*.

For detailed information about PICs, see the *PIC Guide* for your router.



**NOTE:** When T1 is used for SAToP, the T1 facility data-link (FDL) loop is *not* supported on the CT1 interface device. This is because SAToP does not analyze T1 framing bits.

#### Related Documentation

- [Understanding Mobile Backhaul on page 13](#)
- [Understanding Circuit Emulation Services and the Supported PIC Types on page 3](#)
- [Configuring SAToP on 4-Port Channelized OC3/STM1 Circuit Emulation MICs on page 19](#)

---

## Setting the SAToP Options

To configure SAToP options on T1/E1 interfaces:

1. In configuration mode, go to the **[edit interfaces e1-fpc-slot/pic-slot/port]** hierarchy level.

```
[edit]
user@host# edit interfaces e1-fpc-slot/pic-slot/port
```

For example:

```
[edit]
user@host# edit interfaces e1-1/0/0
```

2. Use the **edit** command to go to the **satop-options** hierarchy level.

```
[edit]
user@host# edit satop-options
```

3. In this hierarchy level, using the **set** command you can configure the following SAToP options:

- **excessive-packet-loss-rate**—Set packet loss options. The options are **groups**, **sample-period**, and **threshold**.
  - **groups**—Specify groups.
  - **sample-period**—Time required to calculate excessive packet loss rate (from 1000 through 65,535 milliseconds).
  - **threshold**—Percentile designating the threshold of excessive packet loss rate (1–100 percent).
- **idle-pattern**—An 8-bit hexadecimal pattern to replace TDM data in a lost packet (from 0 through 255).
- **jitter-buffer-auto-adjust**—Automatically adjust the jitter buffer.



**NOTE:** The **jitter-buffer-auto-adjust** option is not applicable on MX Series routers.

- **jitter-buffer-latency**—Time delay in the jitter buffer (from 1 through 1000 milliseconds).
- **jitter-buffer-packets**—Number of packets in the jitter buffer (from 1 through 64 packets).
- **payload-size**—Configure the payload size, in bytes (from 32 through 1024 bytes).



**NOTE:** In this section, we are configuring only one SAToP option. You can follow the same method to configure all the other SAToP options.

```
[edit interfaces e1-1/0/0 satop-options]
user@host# set excessive-packet-loss-rate sample-period sample-period
```

For example:

```
[edit interfaces e1-1/0/0 satop-options]
user@host# set excessive-packet-loss-rate sample-period 4000
```

To verify this configuration, use the **show** command at the **[edit interfaces e1-1/0/0]** hierarchy level:

```
[edit interfaces e1-1/0/0]
user@host# show
satop-options {
  excessive-packet-loss-rate {
    sample-period 4000;
  }
}
```

**Related** • [satop-options on page 132](#)  
**Documentation**

## CHAPTER 4

# Configuring SAToP Support on Circuit Emulation MICs

- [Configuring SAToP on 16-Port Channelized E1/T1 Circuit Emulation MIC on page 33](#)
- [Configuring SAToP Encapsulation on T1/E1 Interfaces on page 36](#)
- [SAToP Emulation on T1 and E1 Interfaces Overview on page 39](#)
- [Configuring SAToP Emulation on Channelized T1 and E1 Interfaces on page 40](#)

## Configuring SAToP on 16-Port Channelized E1/T1 Circuit Emulation MIC

---

The following sections describes configuring SAToP on the 16-Port Channelized E1/T1 Circuit Emulation MIC (MIC-3D-16CHE1-T1-CE).

- [Configuring T1/E1 Framing Mode at the MIC Level on page 33](#)
- [Configuring CT1 Ports Down to T1 Channels on page 34](#)
- [Configuring CT1 Ports Down to DS Channels on page 35](#)

### Configuring T1/E1 Framing Mode at the MIC Level

To configure the framing emulation mode at the MIC level.

1. Go to the **[edit chassis fpc fpc-slot pic pic-slot]** hierarchy level.

```
[edit]
[edit chassis fpc fpc-slot pic pic-slot]
```

2. Configure the framing emulation mode as E1 or T1.

```
[edit chassis fpc fpc-slot pic pic-slot]
user@host# set framing (t1 | e1)
```

After a MIC is brought online, interfaces are created for the MIC's available ports on the basis of the MIC type and the framing option used:

- If you include the **framing t1** statement, 16 channelized T1 (CT1) interfaces are created.
- If you include the **framing e1** statement, 16 channelized E1 (CE1) interfaces are created.



**NOTE:** If you set the framing option incorrectly for the MIC type, the commit operation fails.

By default, t1 framing mode is selected.

Circuit Emulation PICs with SONET and SDH ports require prior channelization down to T1 or E1 before you can configure them. Only T1/E1 channels support SAToP encapsulation or SAToP options.

Bit error rate test (BERT) patterns with all binary 1s (ones) received by CT1/CE1 interfaces on Circuit Emulation MICs configured for SAToP do not result in an alarm indication signal (AIS) defect. As a result, the CT1/CE1 interfaces remain up.

## Configuring CT1 Ports Down to T1 Channels

To configure a CT1 port down to a T1 channel, use the following procedure:



**NOTE:** To configure a CE1 port down to the E1 channel, replace ct1 with ce1 and t1 with e1 in the procedure.

1. In configuration mode, go to the `[edit interfaces ct1-mpc-slot/mic-slot/port-number]` hierarchy level.

```
[edit]
user@host# edit interfaces ct1-mpc-slot/mic-slot/port-number
```

For example:

```
[edit]
user@host# edit interfaces ct1-1/0/0
```

2. On the CT1 interface, set the **no-partition** option and then set the interface type as T1.

```
[edit interfaces ct1-mpc-slot/mic-slot/port-number]
user@host# set no-partition interface-type t1
```

In the following example, the ct1-1/0/1 interface is configured to be of type T1 and to have no partitions.

```
[edit interfaces ct1-1/0/1]
user@host# set no-partition interface-type t1
```

## Configuring CTI Ports Down to DS Channels

To configure a channelized T1 (CTI) port down to a DS channel, include the **partition** statement at the **[edit interfaces ct1-mpc-slot/mic-slot/port-number]** hierarchy level:



**NOTE:** To configure a CE1 port down to a DS channel, replace ct1 with ce1 in the following procedure.

1. In configuration mode, go to the **[edit interfaces ct1-mpc-slot/mic-slot/port-number]** hierarchy level.

```
[edit]
user@host# edit interfaces ct1-mpc-slot/mic-slot/port-number
```

For example:

```
[edit]
user@host# edit interfaces ct1-1/0/0
```

2. Configure the partition, the time slot, and the interface type.

```
[edit interfaces ct1-mpc-slot/mic-slot/port-number]
user@host# set partition partition-number timeslots timeslots interface-type ds
```

In the following example, the ct1-1/0/0 interface is configured as a DS interface with one partition and three time slots:

```
[edit interfaces ct1-1/0/0]
user@host# set partition 1 timeslots 1-4,9,22-24 interface-type ds
```

To verify the configuration of the ct1-1/0/0 interface, use the **show** command at the **[edit interfaces ct1-1/0/0]** hierarchy level.

```
[edit interfaces ct1-1/0/0]
user@host# show
partition 1 timeslots 1-4,9,22-24 interface-type ds;
```

An NxDS0 interface can be configured from channelized T1 interface. Here *N* represents the time slots on the CTI interface. The value of *N* is:

- 1 through 24 when a DS0 interface is configured from a CTI interface.
- 1 through 31 when a DS0 interface is configured from a CE1 interface.

After you partition the DS interface, configure the SAToP options on it. See [“Setting the SAToP Options” on page 27](#).

### Related Documentation

- [Understanding Circuit Emulation Services and the Supported PIC Types on page 3](#)
- [Setting the SAToP Options on page 27](#)

## Configuring SAToP Encapsulation on T1/E1 Interfaces

This configuration applies to the mobile backhaul application shown in [Figure 3 on page 14](#).

This topic includes the following tasks:

- [Setting the Encapsulation Mode on page 36](#)
- [T1/E1 Loopback Support on page 36](#)
- [T1 FDL Support on page 37](#)
- [Setting the SAToP Options on page 37](#)
- [Configuring the Pseudowire Interface on page 38](#)

### Setting the Encapsulation Mode

E1 channels on Circuit Emulation MICs can be configured with SAToP encapsulation at the provider edge (PE) router, as follows:



**NOTE:** The following procedure can be used to configure T1 channels on Circuit Emulation MICs with SAToP encapsulation at the PE router.

1. In configuration mode, go to the **[edit interfaces e1-fpc-slot/pic-slot/port]** hierarchy level.

```
[edit]
user@host# edit interfaces e1-fpc-slot/pic-slot/port
```

For example:

```
[edit]
user@host# edit interfaces e1-1/0/0
```

2. Configure the SAToP encapsulation and the logical interface for E1 interface.

```
[edit interfaces e1-1/0/0]
user@host# set encapsulation satop unit interface-unit-number
```

For example:

```
[edit interfaces e1-1/0/0]
user@host# set encapsulation satop unit 0
```

You do not need to configure any cross-connect circuit family because it is automatically created for the SAToP encapsulation.

### T1/E1 Loopback Support

Use the CLI to configure remote and local loopback as T1 (CT1) or E1 (CE1). By default, no loopback is configured. See [Configuring T1 Loopback Capability](#) and [Configuring E1 Loopback Capability](#).

## T1 FDL Support

If T1 is used for SAToP, the T1 facility data-link (FDL) loop is *not* supported on the CT1 interface device because SAToP does not analyze T1 framing bits.

## Setting the SAToP Options

To configure SAToP options on T1/E1 interfaces:

1. In configuration mode, go to the **[edit interfaces e1-fpc-slot/pic-slot/port]** hierarchy level.

```
[edit]
user@host# edit interfaces e1-fpc-slot/pic-slot/port
```

For example:

```
[edit]
user@host# edit interfaces e1-1/0/0
```

2. Use the **edit** command to go to the **satop-options** hierarchy level.

```
[edit]
user@host# edit satop-options
```

3. In this hierarchy level, using the **set** command you can configure the following SAToP options:

- **excessive-packet-loss-rate**—Set packet loss options. The options are **groups**, **sample-period**, and **threshold**.
  - **groups**—Specify groups.
  - **sample-period**—Time required to calculate excessive packet loss rate (from 1000 through 65,535 milliseconds).
  - **threshold**—Percentile designating the threshold of excessive packet loss rate (1–100 percent).
- **idle-pattern**—An 8-bit hexadecimal pattern to replace TDM data in a lost packet (from 0 through 255).
- **jitter-buffer-auto-adjust**—Automatically adjust the jitter buffer.



**NOTE:** The **jitter-buffer-auto-adjust** option is not applicable on MX Series routers.

- **jitter-buffer-latency**—Time delay in the jitter buffer (from 1 through 1000 milliseconds).
- **jitter-buffer-packets**—Number of packets in the jitter buffer (from 1 through 64 packets).
- **payload-size**—Configure the payload size, in bytes (from 32 through 1024 bytes).



**NOTE:** In this section, we are configuring only one SAToP option. You can follow the same method to configure all the other SAToP options.

```
[edit interfaces e1-1/0/0 satop-options]
user@host# set excessive-packet-loss-rate sample-period sample-period
```

For example:

```
[edit interfaces e1-1/0/0 satop-options]
user@host# set excessive-packet-loss-rate sample-period 4000
```

To verify this configuration, use the **show** command at the **[edit interfaces e1-1/0/0]** hierarchy level:

```
[edit interfaces e1-1/0/0]
user@host# show
satop-options {
  excessive-packet-loss-rate {
    sample-period 4000;
  }
}
```

See Also • [satop-options on page 132](#)

## Configuring the Pseudowire Interface

To configure the TDM pseudowire at the provider edge (PE) router, use the existing Layer 2 circuit infrastructure, as shown in the following procedure:

1. In configuration mode, go to the **[edit protocols l2circuit]** hierarchy level.

```
[edit]
user@host# edit protocol l2circuit
```

2. Configure the IP address of the neighboring router or switch, the interface forming the Layer 2 circuit, and the identifier for the Layer 2 circuit.

```
[edit protocol l2circuit]
user@host# set neighbor ip-address interface
interface-name-fpc-slot/pic-slot/port.interface-unit-number virtual-circuit-id
virtual-circuit-id
```



**NOTE:** To configure the T1 interface as the Layer 2 circuit, replace **e1** with **t1** in the configuration statement.

For example:

```
[edit protocol l2circuit]
user@host# set neighbor 10.255.0.6 interface e1-1/0/0.0 virtual-circuit-id 1
```

3. To verify this configuration, use the **show** command at the **[edit protocols l2circuit]** hierarchy level.

```
[edit protocols l2circuit]
user@host# show
neighbor 10.255.0.6 {
  interface e1-1/0/0.0 {
    virtual-circuit-id 1;
  }
}
```

After the customer edge (CE)-bound interfaces (for both PE routers) are configured with proper encapsulation, payload size, and other parameters, the two PE routers try to establish a pseudowire with Pseudowire Emulation Edge-to-Edge (PWE3) signaling extensions. The following pseudowire interface configurations are disabled or ignored for TDM pseudowires:

- **ignore-encapsulation**
- **mtu**

The supported pseudowire types are:

- 0x0011 Structure-Agnostic E1 over Packet
- 0x0012 Structure-Agnostic T1 (DS1) over Packet

When the local interface parameters match the received parameters, and the pseudowire type and control word bit are equal, the pseudowire is established.

For detailed information about configuring TDM pseudowire, see the *Junos OS VPNs Library for Routing Devices*.

For detailed information about MICs, see the *PIC Guide* for your router.

#### Related Documentation

- [Understanding Mobile Backhaul on page 13](#)

## SAToP Emulation on T1 and E1 Interfaces Overview

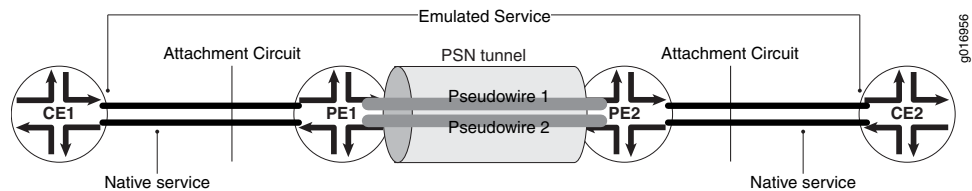
Structure-Agnostic time-division multiplexing (TDM) over Packet (SAToP), as defined in RFC 4553, *Structure-Agnostic TDM over Packet (SAToP)* is supported on the ACX Series Universal Metro routers with built-in T1 and E1 interfaces. SAToP is used for pseudowire encapsulation for TDM bits (T1, E1). The encapsulation disregards any structure imposed on the T1 and E1 streams, in particular the structure imposed by standard TDM framing. SAToP is used over packet-switched networks, where the provider edge (PE) routers do not need to interpret TDM data or participate in the TDM signaling.



**NOTE:** ACX5048 and ACX5096 routers do not support SAToP.

Figure 5 on page 40 shows a packet-switched network (PSN) in which two PE routers (PE1 and PE2) provide one or more pseudowires to customer edge (CE) routers (CE1 and CE2), establishing a PSN tunnel to provide a data path for the pseudowire.

Figure 5: Pseudowire Encapsulation with SAToP



Pseudowire traffic is invisible to the core network, and the core network is transparent to the CEs. Native data units (bits, cells, or packets) arrive via the attachment circuit, are encapsulated in a pseudowire protocol data unit (PDU), and carried across the underlying network via the PSN tunnel. The PEs perform the necessary encapsulation and the decapsulation of the pseudowire PDUs and handle any other function required by the pseudowire service, such as sequencing or timing.

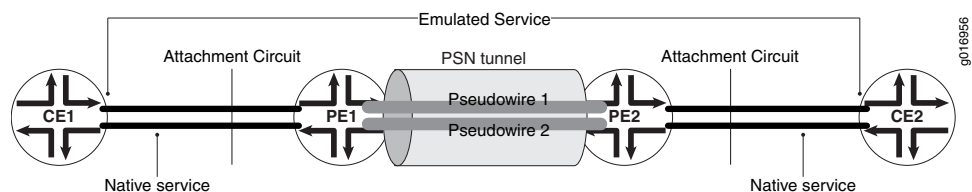
**Related Documentation** • [Configuring SAToP Emulation on Channelized T1 and E1 Interfaces on page 40](#)

## Configuring SAToP Emulation on Channelized T1 and E1 Interfaces

This configuration is the base configuration of SAToP on an ACX Series router as described in RFC 4553, *Structure-Agnostic Time Division Multiplexing (TDM) over Packet (SAToP)*. When you configure SAToP on built-in channelized T1 and E1 interfaces, the configuration results in a pseudowire that acts as a transport mechanism for the T1 and E1 circuit signals across a packet-switched network.

The network between the customer edge (CE) routers appears transparent to the CE routers, making it seem that the CE routers are directly connected. With the SAToP configuration on the provider edge (PE) router's T1 and E1 interfaces, the interworking function (IWF) forms a payload (frame) that contains the CE router's T1 and E1 Layer 1 data and control word. This data is transported to the remote PE over the pseudowire. The remote PE removes all the Layer 2 and MPLS headers added in the network cloud and forwards the control word and the Layer 1 data to the remote IWF, which in turn forwards the data to the remote CE.

Figure 6: Pseudowire Encapsulation with SAToP



In [Figure 6 on page 40](#) the Provider Edge (PE) router represents the ACX Series router that is being configured in these steps. The result of these steps is the pseudowire from PE1 to PE2. Topics include:

- [Setting the T1/E1 Emulation Mode on page 41](#)
- [Configuring One Full T1 or E1 Interface on Channelized T1 and E1 Interfaces on page 42](#)
- [Setting the SAToP Encapsulation Mode on page 45](#)
- [Configure the Layer 2 Circuit on page 45](#)

## Setting the T1/E1 Emulation Mode

Emulation is a mechanism that duplicates the essential attributes of a service (such as T1 or E1) over a packet-switched network. You set the emulation mode so that the built-in channelized T1 and E1 interfaces on the ACX Series router can be configured to work in either T1 or E1 mode. This configuration is at the PIC level, so all ports operate as either T1 interfaces or E1 interfaces. A mix of T1 and E1 interfaces is not supported. By default all the ports operate as T1 interfaces.

- Configure the emulation mode:

```
[edit chassis fpc fpc-slot pic pic-slot]
user@host# set framing (t1 | e1)
```

For example:

```
[edit chassis fpc 0 pic 0]
user@host# set framing t1
```

After a PIC is brought online and depending on the framing option used (**t1** or **e1**), on the ACX2000 router, 16 CT1 or 16 CE1 interfaces are created, and on the ACX1000 router, 8 CT1 or 8 CE1 interfaces are created.

The following output shows this configuration:

```
user@host# show chassis
fpc 0 {
  pic 0 {
    framing t1;
  }
}
```

The following output from the **show interfaces terse** command shows the 16 CT1 interfaces created with the framing configuration.

```
user@host# run show interfaces terse
```

Interface	Admin	Link	Proto	Local	Remote
ct1-0/0/0	up	down			
ct1-0/0/1	up	down			
ct1-0/0/2	up	down			
ct1-0/0/3	up	down			
ct1-0/0/4	up	down			
ct1-0/0/5	up	down			
ct1-0/0/6	up	down			
ct1-0/0/7	up	down			
ct1-0/0/8	up	down			
ct1-0/0/9	up	down			

ct1-0/0/10	up	down
ct1-0/0/11	up	down
ct1-0/0/12	up	down
ct1-0/0/13	up	down
ct1-0/0/14	up	down
ct1-0/0/15	up	down



**NOTE:** If you set the framing option incorrectly for the PIC type, the commit operation fails.

If you change the mode, the router will reboot the built-in T1 and E1 interfaces.

Bit error rate test (BERT) patterns with all ones received by T1 and E1 interfaces configured for SAToP do not result in an alarm indication signal (AIS) defect. As a result, the T1 and E1 interfaces remain up.

- See Also**
- [SAToP Emulation on T1 and E1 Interfaces Overview on page 39](#)
  - *Obsolete: Inverse Multiplexing for ATM (IMA) Overview*

## Configuring One Full T1 or E1 Interface on Channelized T1 and E1 Interfaces

You must configure a child T1 or E1 interface on the built-in channelized T1 or E1 interface created because the channelized interface is not a configurable interface and SAToP encapsulation must be configured (in the next step) for the pseudowire to function. The following configuration creates one full T1 interface on the channelized **ct1** interface. You can follow the same process to create one E1 interface on the channelized **ce1** interface.

- Configure one full T1/E1 interface:

```
[edit interfaces ct1-fpc/pic /port]
user@host# set no-partition interface-type (t1 | e1)
```

For example:

```
[edit interfaces ct1-0/0/0]
user@host# set no-partition interface-type t1
```

The following output shows this configuration:

```
[edit]
user@host# show interfaces
ct1-0/0/0 {
    no-partition interface-type t1;
}
```

The preceding command creates the **t1-0/0/0** interface on the channelized **ct1-0/0/0** interface. Check the configuration with the **show interfaces *interface-name* extensive** command. Run the command to display output for the channelized interface and the newly created T1 or E1 interface. The following output provides an example of the output

for a CT1 interface and the T1 interface created from the preceding example configuration. Notice that **ct1-0/0/0** is running at T1 speed and that the media is T1.

```

user@host> show interfaces ct1-0/0/0 extensive
Physical interface: ct1-0/0/0, Enabled, Physical link is Up
  Interface index: 152, SNMP ifIndex: 780, Generation: 1294
  Link-level type: Controller, Clocking: Internal, Speed: T1, Loopback: None,
  Framing: ESF, Parent: None
  Device flags   : Present Running
  Interface flags: Point-To-Point SNMP-Traps Internal: 0x0
  Link flags     : None
  Hold-times     : Up 0 ms, Down 0 ms
  CoS queues     : 8 supported, 4 maximum usable queues
  Last flapped   : 2012-04-03 06:27:55 PDT (00:13:32 ago)
  Statistics last cleared: 2012-04-03 06:40:34 PDT (00:00:53 ago)
  DS1 alarms    : None
  DS1 defects    : None
  T1 media:
    Seconds      Count  State
    SEF          0       0 OK
    BEE          0       0 OK
    AIS          0       0 OK
    LOF          0       0 OK
    LOS          0       0 OK
    YELLOW       0       0 OK
    CRC Major    0       0 OK
    CRC Minor    0       0 OK
    BPV          0       0
    EXZ          0       0
    LCV          0       0
    PCV          0       0
    CS           0       0
    CRC          0       0
    LES          0
    ES           0
    SES          0
    SEFS         0
    BES          0
    UAS          0
  Line encoding: B8ZS
  Buildout      : 0 to 132 feet
  DS1 BERT configuration:
    BERT time period: 10 seconds, Elapsed: 0 seconds
    Induced Error rate: 0, Algorithm: 2^15 - 1, 0.151, Pseudorandom (9)
  Packet Forwarding Engine configuration:
    Destination slot: 0 (0x00)

```

In the following output for the T1 interface, the parent interface is shown as **ct1-0/0/0** and the link level type and encapsulation are **TDM-CCC-SATOP**.

```

user@host> show interfaces t1-0/0/0 extensive
Physical interface: t1-0/0/0, Enabled, Physical link is Up
  Interface index: 160, SNMP ifIndex: 788, Generation: 1302
  Link-level type: TDM-CCC-SATOP, MTU: 1504, Speed: T1, Loopback: None, FCS:
  16, Parent: ct1-0/0/0 Interface index 152
  Device flags   : Present Running
  Interface flags: Point-To-Point SNMP-Traps Internal: 0x0
  Link flags     : None
  Hold-times     : Up 0 ms, Down 0 ms
  CoS queues     : 8 supported, 4 maximum usable queues

```

```

Last flapped   : 2012-04-03 06:28:43 PDT (00:01:16 ago)
Statistics last cleared: 2012-04-03 06:29:58 PDT (00:00:01 ago)
Egress queues: 8 supported, 4 in use
Queue counters:      Queued packets  Transmitted packets      Dropped packets

    0 best-effort                0                0                0
    1 expedited-fo                0                0                0
    2 assured-forw                0                0                0
    3 network-cont                0                0                0

Queue number:      Mapped forwarding classes
    0                best-effort
    1                expedited-forwarding
    2                assured-forwarding
    3                network-control

DS1  alarms   : None
DS1  defects  : None
SAtop configuration:
  Payload size: 192
  Idle pattern: 0xFF
  Octet aligned: Disabled
  Jitter buffer: packets: 8, latency: 7 ms, auto adjust: Disabled
  Excessive packet loss rate: sample period: 10000 ms, threshold: 30%
Packet Forwarding Engine configuration:
  Destination slot: 0
CoS information:
  Direction : Output
  CoS transmit queue      Bandwidth      Buffer Priority
Limit                    %      bps      %      usec      low
    0 best-effort        95      1459200    95      0      low
none
    3 network-control     5       76800     5      0      low
none

Logical interface t1-0/0/0.0 (Index 308) (SNMP ifIndex 789) (Generation 11238)

  Flags: Point-To-Point SNMP-Traps Encapsulation: TDM-CCC-SATOP
CE info      Packets      Bytes      Count
CE Tx        0            0
CE Rx        0            0
CE Rx Forwarded      0
CE Strayed      0
CE Lost        0
CE Malformed    0
CE Misinserted  0
CE AIS dropped   0
CE Dropped      0            0
CE Overrun Events      0
CE Underrun Events    0
Protocol ccc, MTU: 1504, Generation: 13130, Route table: 0

```

## Setting the SAToP Encapsulation Mode

The built-in T1 and E1 interfaces must be configured with SAToP encapsulation at the PE router so that the interworking function (IWF) can segment and encapsulate TDM signals into SAToP packets, and in the reverse direction, to decapsulate the SAToP packets and reconstitute them into TDM signals.

1. On the PE router, configure SAToP encapsulation on the physical interface:

```
[edit interfaces (t1 | e1) -fpc/pic /port]
user@host# set encapsulation satop
```

For example:

```
[edit interfaces t1-0/0/0]
user@host# set encapsulation satop
```

2. On the PE router, configure the logical interface:

```
[edit interfaces ]
user@host# set (t1 | e1) -fpc/pic/port unit logical-unit-number
```

For example:

```
[edit interfaces]
user@host# set t1-0/0/0 unit 0
```

It is not necessary to configure the circuit cross-connect (CCC) family because it is automatically created for the preceding encapsulation. The following output shows this configuration.

```
[edit interfaces]
user@host# show t1-0/0/0
encapsulation satop;
unit 0;
```

## Configure the Layer 2 Circuit

When you configure the Layer 2 circuit, you designate the neighbor for the provider edge (PE) router. Each Layer 2 circuit is represented by the logical interface connecting the local PE router to the local customer edge (CE) router. All the Layer 2 circuits that use a particular remote PE router, designated for remote CE routers, are listed under the **neighbor** statement. Each neighbor is identified by its IP address and is usually the end-point destination for the label-switched path (LSP) tunnel that transports the Layer 2 circuit. Configure the Layer 2 circuit:

- [edit protocols l2circuit neighbor *address*]
 

```
user@host# set interface interface-name virtual-circuit-id identifier
```

For example, for a T1 interface:

```
[edit protocols l2circuit neighbor 2.2.2.2]
user@host# set interface t1-0/0/0.0 virtual-circuit-id 1
```

The preceding configuration is for a T1 interface. To configure an E1 interface, use the E1 interface parameters. The following output shows this configuration.

```
[edit protocols l2circuit]
user@host# show neighbor 2.2.2.2
interface t1-0/0/0.0 {
    virtual-circuit-id 1;
}
```

- See Also**
- *Configuring Interfaces for Layer 2 Circuits Overview*
  - *Enabling the Layer 2 Circuit When the MTU Does Not Match*

## CHAPTER 5

# Configuring CESoPSN Support on Circuit Emulation MIC

- [TDM CESoPSN Overview on page 47](#)
- [Configuring TDM CESoPSN on ACX Series Routers Overview on page 48](#)
- [Configuring CESoPSN on Channelized E1/T1 Circuit Emulation MIC on page 49](#)
- [Configuring CESoPSN on Channelized OC3/STM1 \(Multi-Rate\) Circuit Emulation MIC with SFP on page 53](#)
- [Configuring CESoPSN Encapsulation on DS Interfaces on page 62](#)
- [Configuring CE1 Channels Down to DS Interfaces on page 66](#)
- [Configuring CESoPSN on Channelized E1/T1 Circuit Emulation MIC on ACX Series on page 68](#)

## TDM CESoPSN Overview

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Circuit Emulation Service over Packet-Switched Network (CESoPSN) is an encapsulation layer intended to carry *NxDS0* services over a packet-switched network (PSN). CESoPSN enables pseudowire emulation of some properties of structure-aware time division multiplexed (TDM) networks.

Particularly, CESoPSN enables the deployment of bandwidth-saving fractional point-to-point E1 or T1 applications as follows:

- A pair of customer edge (CE) devices operate as though they were connected by an emulated E1 or T1 circuit, which reacts to the alarm indication signal (AIS) and remote alarm indication (RAI) states of the devices' local attachment circuits.
- The PSN carries only an *NxDS0* service, where *N* is the number of actually used time slots in the circuit connecting the pair of CE devices, thus saving bandwidth.

### Related Documentation

- [Configuring TDM CESoPSN on ACX Series Routers Overview on page 48](#)
- [Configuring CESoPSN Encapsulation on DS Interfaces](#)
- [Configuring CE1 Channels Down to DS Interfaces on page 66](#)

## Configuring TDM CESoPSN on ACX Series Routers Overview

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Structure-aware time division multiplexed (TDM) Circuit Emulation Service over Packet-Switched Network (CESoPSN) is a method of encapsulating TDM signals into CESoPSN packets, and in the reverse direction, decapsulating CESoPSN packets back into TDM signals. This method is also termed as *Interworking Function* (IWF). The following CESoPSN features are supported on Juniper Networks ACX Series Universal Metro Routers:

- [Channelization up to the DS0 Level on page 48](#)
- [Protocol Support on page 48](#)
- [Packet Latency on page 48](#)
- [CESoPSN Encapsulation on page 49](#)
- [CESoPSN Options on page 49](#)
- [show Commands on page 49](#)
- [CESoPSN Pseudowires on page 49](#)

### Channelization up to the DS0 Level

The following numbers of  $N \times \text{DS0}$  pseudowires are supported for 16 T1 and E1 built-in ports and 8 T1 and E1 built-in ports, where  $N$  represents the time slots on the T1 and E1 built-in ports.

16 T1 and E1 built-in ports support the following number of pseudowires:

- Each T1 port can have up to 24  $N \times \text{DS0}$  pseudowires, which add up to a total of up to 384  $N \times \text{DS0}$  pseudowires.
- Each E1 port can have up to 31  $N \times \text{DS0}$  pseudowires, which add up to a total of up to 496  $N \times \text{DS0}$  pseudowires.

8 T1 and E1 built-in ports support the following number of pseudowires:

- Each T1 port can have up to 24  $N \times \text{DS0}$  pseudowires, which add up to a total of up to 192  $N \times \text{DS0}$  pseudowires.
- Each E1 port can have up to 31  $N \times \text{DS0}$  pseudowires, which add up to a total of up to 248  $N \times \text{DS0}$  pseudowires.

### Protocol Support

All protocols that support Structure-Agnostic TDM over Packet (SAToP) support CESoPSN  $N \times \text{DS0}$  interfaces.

### Packet Latency

The time required to create packets (from 1000 through 8000 microseconds).

## CESoPSN Encapsulation

The following statements are supported at the [edit interfaces *interface-name*] hierarchy level:

- `ct1-x/y/z partition partition-number timeslots timeslots interface-type ds`
- `ds-x/y/z:n encapsulation cesopsn`

## CESoPSN Options

The following statements are supported at the [edit interfaces *interface-name* cesopsn-options] hierarchy level:

- `excessive-packet-loss-rate (sample-period milliseconds)`
- `idle-pattern pattern`
- `jitter-buffer-latency milliseconds`
- `jitter-buffer-packets packets`
- `packetization-latency microseconds`

## show Commands

The `show interfaces interface-name extensive` command is supported for **t1**, **e1**, and **at** interfaces.

## CESoPSN Pseudowires

CESoPSN pseudowires are configured on the logical interface, not on the physical interface. So the `unit logical-unit-number` statement must be included in the configuration at the [edit interfaces *interface-name*] hierarchy level. When you include the `unit logical-unit-number` statement, circuit cross-connect (CCC) for the logical interface is created automatically.

### Related Documentation

- [Setting the CESoPSN Options on page 51](#)

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## Configuring CESoPSN on Channelized E1/T1 Circuit Emulation MIC

To configure Circuit Emulation Service over Packet-Switched Network (CESoPSN) protocol on a 16-port Channelized E1/T1 Circuit Emulation MIC (MIC-3D-16CHE1-T1-CE), you must configure the framing mode, configure CT1 interface down to DS channels, and configure the CESoPSN encapsulation on DS interfaces.

- [Configuring T1/E1 Framing Mode at the MIC Level on page 50](#)
- [Configuring CT1 Interface Down to DS Channels on page 50](#)
- [Setting the CESoPSN Options on page 51](#)
- [Configuring CESoPSN on DS Interfaces on page 52](#)

## Configuring T1/E1 Framing Mode at the MIC Level

To set the framing mode at the MIC (MIC-3D-16CHE1-T1-CE) level, for all four ports on the MIC, include the **framing** statement at the **[edit chassis fpc slot pic slot]** hierarchy level.

```
[edit chassis fpc slot pic slot]
user@host# set framing (t1 | e1);
```

After a MIC is brought online, interfaces are created for the MIC's available ports on the basis of the MIC type and the framing option used.

- If you include the **framing t1** statement, 16 CT1 interfaces are created.
- If you include the **framing e1** statement, 16 CE1 interfaces are created.



**NOTE:** If you set the framing option incorrectly for the MIC type, the commit operation fails.

Bit error rate test (BERT) patterns with all binary 1s (ones) received by CT1/CE1 interfaces on Circuit Emulation MICs configured for CESoPSN do not result in an alarm indication signal (AIS) defect. As a result, the CT1/CE1 interfaces remain up.

## Configuring CT1 Interface Down to DS Channels

To configure a channelized T1 (CT1) interface down to DS channels, include the **partition** statement at the **[edit interfaces ct1-mpc-slot/mic-slot/port-number]** hierarchy level:



**NOTE:** To configure a CE1 interface down to DS channels, replace ct1 with ce1 in the following procedure.

1. In configuration mode, go to the **[edit interfaces ct1-mpc-slot/mic-slot/port-number]** hierarchy level.

```
[edit]
user@host# edit interfaces ct1-mpc-slot/mic-slot/port-number
```

For example:

```
[edit]
user@host# edit interfaces ct1-1/0/0
```

2. Configure the sublevel interface partition index and the time slots, and set the interface type as **ds**.

```
[edit interfaces ct1-mpc-slot/mic-slot/port-number]
user@host# set partition partition-number timeslots timeslots interface-type ds
```

For example:

```
[edit interfaces ct1-1/0/0]
user@host# set partition 1 timeslots 1-4 interface-type ds
```



**NOTE:** You can assign multiple time slots on a CT1 interface. In the `set` command, separate the time slots by commas and do not include spaces between them. For example:

```
[edit interfaces ct1-1/0/0]
user@host# set partition 1 timeslots 1-4,9,22-24 interface-type ds
```

To verify this configuration, use the `show` command at the `[edit interfaces ct1-1/0/0]` hierarchy level.

```
[edit interfaces ct1-1/0/0]
user@host# show
partition 1 timeslots 1-4 interface-type ds;
```

An  $N \times$ DS0 interface can be configured from a CT1 interface. Here  $N$  represents the number of time slots on the CT1 interface. The value of  $N$  is:

- 1 through 24 when a DS0 interface is configured from a CT1 interface.
- 1 through 31 when a DS0 interface is configured from a CE1 interface.

After you partition the DS interface, configure CESoPSN options on it.

## Setting the CESoPSN Options

To configure CESoPSN options:

1. In configuration mode, go to the `[edit interfaces ds-fpc-slot/pic-slot/port:channel]` hierarchy level.

```
[edit]
user@host# edit interfaces ds-fpc-slot/pic-slot/port:channel
```

For example:

```
[edit]
user@host# edit interfaces ds-1/0/0:1:1
```

2. Use the `edit` command to go to the `[edit cesopsn-options]` hierarchy level.

```
[edit interfaces ds-fpc-slot/pic-slot/port:channel]
user@host# edit cesopsn-options
```

3. Configure the following CESoPSN options:



**NOTE:** When you stitch pseudowires by using interworking (iw) interfaces, the device stitching the pseudowire cannot interpret the characteristics of the circuit because the circuits originate and terminate in other nodes. To negotiate between the stitching point and circuit endpoints, you need to configure the following options.

- **excessive-packet-loss-rate**—Set packet loss options. The options are **sample-period** and **threshold**.

```
[edit interfaces ds-fpc-slot/pic-slot/port:channel cesopsn-options]
user@host# set excessive-packet-loss-rate sample-period sample-period
```

- **idle-pattern**—An 8-bit hexadecimal pattern to replace TDM data in a lost packet (from 0 through 255).
- **jitter-buffer-latency**—Time delay in the jitter buffer (from 1 through 1000 milliseconds).
- **jitter-buffer-packets**—Number of packets in the jitter buffer (from 1 through 64 packets).
- **packetization-latency**—Time required to create packets (from 1000 through 8000 microseconds).
- **payload-size**—Payload size for virtual circuits that terminate on Layer 2 interworking (iw) logical interfaces (from 32 through 1024 bytes).

To verify the configuration using the values shown in the examples, use the **show** command at the **[edit interfaces ds-1/0/0:1:1:1]** hierarchy level:

```
[edit interfaces ds-1/0/0:1:1:1]
user@host# show
cesopsn-options {
  excessive-packet-loss-rate {
    sample-period 4000;
  }
}
```

- See Also**
- [Setting the Encapsulation Mode on page 63](#)
  - [Configuring the Pseudowire Interface on page 65](#)

## Configuring CESoPSN on DS Interfaces

To configure CESoPSN encapsulation on a DS interface, include the **encapsulation** statement at the **[edit interfaces ds-mpc-slot/mic-slot/port-number:channel]** hierarchy level.

1. In configuration mode, go to the **[edit interfaces ds-mpc-slot/mic-slot/port-number:channel]** hierarchy level.

```
[edit]
user@host# edit interfaces ds-mpc-slot/mic-slot/ port-number:channel
```

For example:

```
[edit]
user@host# edit interfaces ds-1/0/0:1
```

2. Configure CESoPSN as the encapsulation type.

```
[edit interfaces ds-mpc-slot/mic-slot/port-number:partition ]
user@host# set encapsulation cesopsn
```

For example:

```
[edit interfaces ds-1/0/0:1 ]
user@host# set encapsulation cesopsn
```

3. Configure the logical interface for the DS interface.

```
[edit interfaces ds-mpc-slot/mic-slot/port-number:partition ]
uset@host# set unit interface-unit-number
```

For example:

```
[edit interfaces ds-1/0/0:1 ]
user@host# set unit 0
```

To verify this configuration, use the **show** command at the **[edit interfaces ds-1/0/0:1]** hierarchy level.

```
[edit interfaces ds-1/0/0:1]
user@host# show
encapsulation cesopsn;
unit 0;
```

#### Related Documentation

- [Understanding Circuit Emulation Services and the Supported PIC Types on page 3](#)

## Configuring CESoPSN on Channelized OC3/STM1 (Multi-Rate) Circuit Emulation MIC with SFP

To configure CESoPSN options on a Channelized OC3/STM1 (Multi-Rate) Circuit Emulation MIC with SFP, you must configure the speed and framing mode at MIC level and configure the encapsulation as CESoPSN on DS interfaces.

- [Configuring SONET/SDH Rate-Selectability on page 53](#)
- [Configuring SONET/SDH Framing Mode at the MIC Level on page 54](#)
- [Configuring CESoPSN Encapsulation on DS Interfaces on CT1 Channels on page 55](#)
- [Configuring CESoPSN Encapsulation on DS Interfaces on CE1 Channels on page 58](#)

### Configuring SONET/SDH Rate-Selectability

You can configure rate-selectability on the Channelized OC3/STM1 (Multi-Rate) MICs with SFP(MIC-3D-4COC3-1COC12-CE) by specifying the port speed. The Channelized

OC3/STM1 (Multi-Rate) Circuit Emulation MIC with SFP is rate-selectable and its port speed can be specified as **COC3-CSTM1** or **COC12-CSTM4**.

To configure port speed to select a speed option of **coc3-cstm1** or **coc12-cstm4**:

1. In configuration mode, go to the **[edit chassis fpc slot pic slot port slot]** hierarchy level.

```
[edit]
user@host# edit chassis fpc slot pic slot port slot
```

For example:

```
[edit]
user@host# edit chassis fpc 1 pic 0 port 0
```

2. Set the speed as **coc3-cstm1** or **coc12-cstm4**.

```
[edit chassis fpc slot pic slot port slot]
user@host# set speed (coc3-cstm1 | coc12-cstm4)
```

For example:

```
[edit chassis fpc 1 pic 0 port 0]
user@host# set speed coc3-cstm1
```



**NOTE:** When the speed is set as **coc12-cstm4**, instead of configuring **COC3** ports down to T1 channels and **CSTM1** ports down to E1 channels, you must configure **COC12** ports down to T1 channels and **CSTM4** channels down to E1 channels.

---

## Configuring SONET/SDH Framing Mode at the MIC Level

To set the framing mode at the MIC (MIC-3D-4COC3-1COC12-CE) level, for all four ports on the MIC, include the **framing** statement at the **[edit chassis fpc slot pic slot]** hierarchy level.

```
[edit chassis fpc slot pic slot]
user@host# set framing (sonet | sdh) # SONET for COC3/COC12 or SDH for CSTM1/CSTM4
```

After a MIC is brought online, interfaces are created for the MIC's available ports on the basis of the MIC type and the framing option used.

- If you include the **framing sonet** statement, four **COC3** interfaces are created when the speed is configured as **coc3-cstm1**.
- If you include the **framing sdh** statement, four **CSTM1** interfaces are created when the speed is configured as **coc3-cstm1**.
- If you include the **framing sonet** statement, one **COC12** interface is created when the speed is configured as **coc12-cstm4**.

- If you include the **framing sdh** statement, one CSTM4 interface is created when the speed is configured as **coc12-cstm4**.
- If you do not specify framing at the MIC level, then the default framing is SONET for all the ports.



**NOTE:** If you set the framing option incorrectly for the MIC type, the commit operation fails.

Bit error rate test (BERT) patterns with all binary 1s (ones) received by CT1/CE1 interfaces on Circuit Emulation MICs configured for CESoPSN do not result in an alarm indication signal (AIS) defect. As a result, the CT1/CE1 interfaces remain up.

## Configuring CESoPSN Encapsulation on DS Interfaces on CT1 Channels

This topic includes the following tasks:

1. [Configuring COC3 Ports Down to CT1 Channels on page 55](#)
2. [Configuring CT1 Channels Down to DS Interfaces on page 56](#)
3. [Configuring CESoPSN on DS Interfaces on page 57](#)

### Configuring COC3 Ports Down to CT1 Channels

When configuring COC3 ports down to CT1 channels, on any MIC configured for SONET framing (numbered 0 through 3), you can configure three COC1 channels (numbered 1 through 3). On each COC1 channel, you can configure a maximum of 28 CT1 channels and a minimum of 1 CT1 channel based on the time slots.

When configuring COC12 ports down to CT1 channels on a MIC configured for SONET framing, you can configure 12 COC1 channels (numbered 1 through 12). On each COC1 channel, you can configure 24 CT1 channels (numbered 1 through 28).

To configure COC3 channelization down to COC1 and then down to CT1 channels, include the **partition** statement at the **[edit interfaces (coc1 | coc3)-mpc-slot/mic-slot/port-number]** hierarchy level:



**NOTE:** To configure COC12 ports down to CT1 channels, replace **coc3** with **coc12** in the following procedure.

1. In configuration mode, go to the **[edit interfaces coc3-mpc-slot/mic-slot/port-number]** hierarchy level.

```
[edit]
user@host# edit interfaces coc3-mpc-slot/mic-slot/port-number
```

For example:

```
[edit]
user@host# edit interfaces coc3-1/0/0
```

2. Configure the sublevel interface partition index and the range of SONET/SDH slices, and set the sublevel interface type as **coc1**.

```
[edit interfaces coc3-mpc-slot/mic-slot/port-number]
user@host# set partition partition-number oc-slice oc-slice interface-type coc1
```

For example:

```
[edit interfaces coc3-1/0/0]
user@host# set partition 1 oc-slice 1 interface-type coc1
```

3. Enter the **up** command to go to the **[edit interfaces]** hierarchy level.

```
[edit interfaces coc3-mpc-slot/mic-slot/port-number]
user@host# up
```

For example:

```
[edit interfaces coc3-1/0/0]
user@host# up
```

4. Configure the channelized OC1 interface and the sublevel interface partition index, and set the interface type as **ct1**.

```
[edit interfaces]
user@host# set coc1-1/0/0:1 partition partition-number interface-type ct1
```

For example:

```
[edit interfaces]
user@host# set coc1-1/0/0:1 partition 1 interface-type ct1
```

To verify the configuration, use the **show** command at the **[edit interfaces]** hierarchy level.

```
[edit interfaces]
user@host# show
coc3-1/0/0 {
  partition 1 oc-slice 1 interface-type coc1;
}
coc1-1/0/0:1 {
  partition 1 interface-type ct1;
}
```

---

### Configuring CT1 Channels Down to DS Interfaces

To configure CT1 channels down to a DS interface, include the **partition** statement at the **[edit interfaces ct1-mpc-slot/mic-slot/port-number:channel:channel]** hierarchy level:

1. In configuration mode, go to the **[edit interfaces ct1-mpc-slot/mic-slot/port-number:channel:channel]** hierarchy level.

```
[edit]
user@host# edit interfaces ct1-mpc-slot/mic-slot/port-number:channel:channel
```

For example:

```
[edit]
```

```
user@host# edit interfaces ct1-1/0/0:1:1
```

2. Configure the partition, the time slots, and the interface type.

```
[edit interfaces ct1-mpc-slot/mic-slot/port-number:channel:channel]
user@host# set partition partition-number timeslots timeslots interface-type ds
```

For example:

```
[edit interfaces ct1-1/0/0:1:1]
user@host# set partition 1 timeslots 1-4 interface-type ds
```



**NOTE:** You can assign multiple time slots on a CT1 interface. In the set command, separate the time slots by commas and do not include spaces between them. For example:

```
[edit interfaces ct1-1/0/0:1:1]
user@host# set partition 1 timeslots 1-4,9,22-24 interface-type ds
```

To verify this configuration, use the **show** command at the **[edit interfaces ct1-1/0/0:1:1]** hierarchy level.

```
[edit interfaces ct1-1/0/0:1:1]
user@host# show
partition 1 timeslots 1-4 interface-type ds;
```

An  $N$ xDS0 interface can be configured from channelized T1 interface (ct1). Here  $N$  represents the time slots on the CT1 interface.

The value of  $N$  is 1 through 24 when a DS0 interface is configured from a CT1 interface.

After you partition the DS interface, configure the CESoPSN options on it. See [“Setting the CESoPSN Options” on page 51](#).

### Configuring CESoPSN on DS Interfaces

To configure CESoPSN encapsulation on a DS interface, include the **encapsulation** statement at the **[edit interfaces ds-mpc-slot/mic-slot/port-number:channel:channel:channel]** hierarchy level.

1. In configuration mode, go to the **[edit interfaces ds-mpc-slot/mic-slot/port-number:channel:channel:channel]** hierarchy level.

```
[edit]
user@host# edit interfaces ds-mpc-slot/mic-slot/port-number:channel:channel:channel
```

For example:

```
[edit]
user@host# edit interfaces ds-1/0/0:1:1:1
```

2. Configure CESoPSN as the encapsulation type and the logical interface for the DS interface.

```
[edit interfaces ds-mpc-slot/mic-slot/port-number:channel:channel:channel]
user@host# set encapsulation cesopsn unit interface-unit-number
```

For example:

```
[edit interfaces ds-1/0/0:1:1:1 ]
user@host# set encapsulation cesopsn unit 0
```

To verify this configuration, use the **show** command at the **[edit interfaces ds-1/0/0:1:1:1]** hierarchy level.

```
[edit interfaces ds-1/0/0:1:1:1]
user@host# show
encapsulation cesopsn;
unit 0;
```

- See Also**
- [Understanding Mobile Backhaul on page 13](#)
  - [Configuring CESoPSN Encapsulation on DS Interfaces on page 62](#)

## Configuring CESoPSN Encapsulation on DS Interfaces on CE1 Channels

This topic includes the following tasks:

- [Configuring CSTM1 Ports Down to CE1 Channels on page 58](#)
- [Configuring CSTM4 Ports Down to CE1 Channels on page 59](#)
- [Configuring CE1 Channels Down to DS Interfaces on page 61](#)
- [Configuring CESoPSN on DS Interfaces on page 62](#)

### Configuring CSTM1 Ports Down to CE1 Channels

---

On any port configured for SDH framing (numbered 0 through 3), you can configure one CAU4 channel. On each CAU4 channel, you can configure 31 CE1 channels (numbered 1 through 31).

To configure CSTM1 channelization down to CAU4 and then down to CE1 channels, include the **partition** statement at the **[edit interfaces (cau4 | cstm1)-mpc-slot/mic-slot/port-number]** hierarchy level, as shown in the following example:

1. In configuration mode, go to the **[edit interfaces cstm1-mpc-slot/mic-slot/port-number]** hierarchy level.

```
[edit]
user@host# edit interfaces cstm1-mpc-slot/mic-slot/port-number
```

For example:

```
[edit]
user@host# edit interfaces cstm1-1/0/1
```

2. On the CSTM1 interface, set the **no-partition** option, and then set the interface type as **cau4**.

```
[edit interfaces cstm1-mpc-slot/mic-slot/port-number]
```

```
user@host# set no-partition interface-type cau4
```

For example:

```
[edit interfaces cstm1-1/0/1]
user@host# set no-partition interface-type cau4
```

3. Enter the **up** command to go to the **[edit interfaces]** hierarchy level.

```
[edit interfaces cstm1-mpc-slot/mic-slot/port-number]
user@host# up
```

For example:

```
[edit interfaces cstm1-1/0/1]
user@host# up
```

4. Configure the MPC slot, the MIC slot, and the port for the CAU4 interface. Set the sublevel interface partition index and set the interface type as **ce1**.

```
[edit interfaces]
user@host# set cau4-mpc-slot/mic-slot/port-number partition partition-number
interface-type ce1
```

For example:

```
[edit interfaces]
user@host# set cau4-1/0/1 partition 1 interface-type ce1
```

To verify this configuration, use the **show** command at the **[edit interfaces]** hierarchy level.

```
[edit interfaces]
user@host# show
cstm1-1/0/1 {
  no-partition interface-type cau4;
}
cau4-1/0/1 {
  partition 1 interface-type ce1;
}
```

### Configuring CSTM4 Ports Down to CE1 Channels



**NOTE:** When the port speed is configured as **coc12-cstm4** at the **[edit chassis fpc slot pic slot port slot]** hierarchy level, you must configure CSTM4 ports down to CE1 channels.

On a port configured for SDH framing, you can configure one CAU4 channel. On the CAU4 channel, you can configure 31 CE1 channels (numbered 1 through 31).

To configure CSTM4 channelization down to CAU4 and then down to CE1 channels, include the **partition** statement at the **[edit interfaces (cau4|cstm4)-mpc-slot/mic-slot/port-number]** hierarchy level.

1. In configuration mode, go to the **[edit interfaces cstm4-mpc-slot/mic-slot/port-number]** hierarchy level.

```
[edit]
user@host# edit interfaces cstm4-mpc-slot/mic-slot/port-number
```

For example:

```
[edit]
user@host# edit interfaces cstm4-1/0/0
```

2. Configure the sublevel interface partition index and the range of SONET/SDH slices, and set the sublevel interface type as **cau4**.

```
[edit interfaces cstm4-1/0/0]
user@host# set partition partition-number oc-slice oc-slice interface-type cau4
```

For **oc-slice**, select from the following ranges: 1–3, 4–6, 7–9, and 10–12.

For **partition**, select a value from 1 through 4.

For example:

```
[edit interfaces cstm4-1/0/0]
user@host# set partition 1 oc-slice 1-3 interface-type cau4
```

3. Enter the **up** command to go to the **[edit interfaces]** hierarchy level.

```
[edit interfaces cstm4-mpc-slot/mic-slot/port-number]
user@host# up
```

For example:

```
[edit interfaces cstm4-1/0/0]
user@host# up
```

4. Configure the MPC slot, the MIC slot, and the port for the CAU4 interface. Set the sublevel interface partition index and set the interface type as **ce1**.

```
[edit interfaces]
user@host# set cau4-mpc-slot/mic-slot/port-number:channel partition
partition-number interface-type ce1
```

For example:

```
[edit interfaces]
user@host# set cau4-1/0/0:1 partition 1 interface-type ce1
```

To verify this configuration, use the **show** command at the **[edit interfaces]** hierarchy level.

```
[edit interfaces]
user@host# show
```

```

cstm4-1/0/0 {
  partition 1 oc-slice 1-3 interface-type cau4;
}
cau4-1/0/0:1 {
  partition 1 interface-type ce1;
}

```

### Configuring CE1 Channels Down to DS Interfaces

To configure CE1 channels down to a DS interface, include the **partition** statement at the **[edit interfaces ce1-mpc-slot/mic-slot/port:channel]** hierarchy level.

1. In configuration mode, go to the **[edit interfaces ce1-mpc-slot/mic-slot/port:channel]** hierarchy level.

```

[edit]
user@host# edit interfaces ce1-mpc-slot/mic-slot/port:channel

[edit]
user@host# edit interfaces ce1-1/0/0:1:1

```

2. Configure the partition and the time slots, and set the interface type as **ds**.

```

[edit interfaces ce1-1/0/0:1:1]
user@host# set partition partition-number timeslots timeslots interface-type ds

```

For example:

```

[edit interfaces ce1-1/0/0:1:1]
user@host# set partition 1 timeslots 1-4 interface-type ds

```



**NOTE:** You can assign multiple time slots on a CE1 interface. In the **set** command, separate the time slots by commas and do not include spaces between them. For example:

```

[edit interfaces ce1-1/0/0:1:1]
user@host# set partition 1 timeslots 1-4,9,22-31 interface-type ds

```

To verify this configuration, use the **show** command at the **[edit interfaces ce1-1/0/0:1:1]** hierarchy level.

```

[edit interfaces ce1-1/0/0:1:1 ]
user@host# show
partition 1 timeslots 1-4 interface-type ds;

```

An  $N \times DS0$  interface can be configured from a channelized E1 interface (CE1). Here  $N$  represents the number of time slots on the CE1 interface.

The value of  $N$  is 1 through 31 when a DS0 interface is configured from a CE1 interface.

After you partition the DS interface, configure the CESoPSN options.

- See Also**
- [Understanding Mobile Backhaul on page 13](#)
  - [Configuring CESoPSN Encapsulation on DS Interfaces on page 62](#)

## Configuring CESoPSN on DS Interfaces

---

To configure CESoPSN encapsulation on a DS interface, include the **encapsulation** statement at the **[edit interfaces ds-mpc-slot/mic-slot/port-number:channel:channel:channel]** hierarchy level.

1. In configuration mode, go to the **[edit interfaces ds-mpc-slot/mic-slot/port-number:channel:channel:channel]** hierarchy level.

```
[edit]
user@host# edit interfaces ds-mpc-slot/mic-slot/port-number:channel:channel:channel
```

For example:

```
[edit]
user@host# edit interfaces ds-1/0/0:1:1:1
```

2. Configure CESoPSN as the encapsulation type and then set the logical interface for the ds interface.

```
[edit interfaces ds-1/0/0:1:1:1 ]
user@host# set encapsulation cesopsn unit interface-unit-number
```

For example:

```
[edit interfaces ds-1/0/0:1:1:1 ]
user@host# set encapsulation cesopsn unit 0
```

To verify this configuration, use the **show** command at the **[edit interfaces ds-1/0/0:1:1:1]** hierarchy level.

```
[edit interfaces ds-1/0/0:1:1:1]
user@host# show
encapsulation cesopsn;
unit 0;
```

### Related Documentation

- [Understanding Mobile Backhaul on page 13](#)
- [Configuring CESoPSN Encapsulation on DS Interfaces on page 62](#)

### Related Documentation

- [Understanding Mobile Backhaul on page 13](#)
- [Configuring CESoPSN Encapsulation on DS Interfaces on page 62](#)

## Configuring CESoPSN Encapsulation on DS Interfaces

---

This configuration applies to the mobile backhaul application shown in [Figure 3 on page 14](#).

1. [Setting the Encapsulation Mode on page 63](#)
2. [Setting the CESoPSN Options on page 63](#)
3. [Configuring the Pseudowire Interface on page 65](#)

## Setting the Encapsulation Mode

To configure a DS interface on Circuit Emulation MICs with CESoPSN encapsulation at the provider edge (PE) router:

1. In configuration mode, go to the **[edit interfaces ds-mpc-slot/mic-slot/port<:channel>]** hierarchy level.

```
[edit]
user@host# edit interfaces ds-mpc-slot/mic-slot/port<:channel>
```

For example:

```
[edit]
user@host# edit interfaces ds-1/0/0:1:1:1
```

2. Configure CESoPSN as the encapsulation type and set the logical interface for the DS interface.

```
[edit interfaces ds-mpc-slot/mic-slot/port<:channel>]
user@host# set encapsulation cesopsn unit logical-unit-number
```

For example:

```
[edit interfaces ds-1/0/0:1:1:1]
user@host# set encapsulation cesopsn unit 0
```

To verify this configuration, use the **show** command at the **[edit interfaces ds-1/0/0:1:1:1]** hierarchy level:

```
[edit interfaces ds-1/0/0:1:1:1]
user@host# show
encapsulation cesopsn;
unit 0;
```

You do not need to configure any circuit cross-connect family because it is automatically created for the CESoPSN encapsulation.

- See Also**
- [Setting the CESoPSN Options on page 51](#)
  - [Configuring the Pseudowire Interface on page 65](#)

## Setting the CESoPSN Options

To configure CESoPSN options:

1. In configuration mode, go to the **[edit interfaces ds-fpc-slot/pic-slot/port:channel]** hierarchy level.

```
[edit]
user@host# edit interfaces ds-fpc-slot/pic-slot/port:channel
```

For example:

```
[edit]
```

```
user@host# edit interfaces ds-1/0/0:1:1
```

2. Use the **edit** command to go to the **[edit cesopsn-options]** hierarchy level.

```
[edit]
```

```
user@host# edit cesopsn-options
```

3. At this hierarchy level, using the **set** command you can configure the following CESoPSN options:



**NOTE:** When you stitch pseudowires by using interworking (iw) interfaces, the device stitching the pseudowire cannot interpret the characteristics of the circuit because the circuits originate and terminate in other nodes. To negotiate between the stitching point and circuit endpoints, you need to configure the following options.

- **excessive-packet-loss-rate**—Set packet loss options. The options are **sample-period** and **threshold**.
  - **sample-period**—Time required to calculate excessive packet loss rate (from 1000 through 65,535 milliseconds).
  - **threshold**—Percentile designating the threshold of excessive packet loss rate (1–100 percent).
- **idle-pattern**—An 8-bit hexadecimal pattern to replace TDM data in a lost packet (from 0 through 255).
- **jitter-buffer-latency**—Time delay in the jitter buffer (from 1 through 1000 milliseconds).
- **jitter-buffer-packets**—Number of packets in the jitter buffer (from 1 through 64 packets).
- **packetization-latency**—Time required to create packets (from 1000 through 8000 microseconds).
- **payload-size**—Payload size for virtual circuits that terminate on Layer 2 interworking (iw) logical interfaces (from 32 through 1024 bytes).



**NOTE:** This topic shows the configuration of only one CESoPSN option. You can follow the same method to configure all the other CESoPSN options.

```
[edit interfaces ds-fpc-slot/pic-slot/port:channel cesopsn-options]  
user@host# set excessive-packet-loss-rate sample-period sample-period
```

For example:

```
[edit interfaces ds-1/0/0:1:1 cesopsn-options]  
user@host# set excessive-packet-loss-rate sample-period 4000
```

To verify the configuration using the values shown in the examples, use the **show** command at the **[edit interfaces ds-1/0/0:1:1]** hierarchy level:

```
[edit interfaces ds-1/0/0:1:1]
user@host# show
cesopsn-options {
  excessive-packet-loss-rate {
    sample-period 4000;
  }
}
```

- See Also**
- [Setting the Encapsulation Mode on page 63](#)
  - [Configuring the Pseudowire Interface on page 65](#)

## Configuring the Pseudowire Interface

To configure the TDM pseudowire at the provider edge (PE) router, use the existing Layer 2 circuit infrastructure, as shown in the following procedure:

1. In configuration mode, go to the **[edit protocols l2circuit]** hierarchy level.

```
[edit]
user@host# edit protocol l2circuit
```

2. Configure the IP address of the neighboring router or switch, the interface forming the Layer 2 circuit, and the identifier for the Layer 2 circuit.

```
[edit protocol l2circuit]
user@host# set neighbor ip-address interface
  interface-name-fpc-slot/pic-slot/port.interface-unit-number virtual-circuit-id
  virtual-circuit-id
```

For example:

```
[edit protocol l2circuit]
user@host# set neighbor 10.255.0.6 interface ds-1/0/0:1:1 virtual-circuit-id 1
```

To verify this configuration, use the **show** command at the **[edit protocols l2circuit]** hierarchy level.

```
[edit protocols l2circuit]
user@host# show
neighbor 10.255.0.6 {
  interface ds-1/0/0:1:1 {
    virtual-circuit-id 1;
  }
}
```

After the customer edge (CE)-bound interfaces (for both PE routers) are configured with proper encapsulation, packetization latency, and other parameters, the two PE routers try to establish a pseudowire with Pseudowire Emulation Edge-to-Edge (PWE3) signaling extensions. The following pseudowire interface configurations are disabled or ignored for TDM pseudowires:

- **ignore-encapsulation**
- **mtu**

The supported pseudowire type is 0x0015 CESoPSN basic mode.

When the local interface parameters match the received parameters, and the pseudowire type and control word bit are equal, the pseudowire is established.

For detailed information about configuring TDM pseudowire, see the *Junos OS VPNs Library for Routing Devices*.

For detailed information about PICs, see the *PIC Guide* for your router.

- See Also**
- [Setting the Encapsulation Mode on page 63](#)
  - [Setting the CESoPSN Options on page 51](#)

- Related Documentation**
- [Configuring CESoPSN on Channelized OC3/STM1 \(Multi-Rate\) Circuit Emulation MIC with SFP on page 53](#)
  - [Understanding Mobile Backhaul on page 13](#)

---

## Configuring CE1 Channels Down to DS Interfaces

---

You can configure a DS interface on a channelized E1 interface (CE1) and then apply CESoPSN encapsulation for the pseudowire to function. An NxDS0 interface can be configured from a channelized CE1 interface, where *N* represents the time slots on the CE1 interface. The value of *N* is 1 through 31 when a DS0 interface is configured from a CE1 interface.

To configure CE1 channels down to a DS interface, include the **partition** statement at the **[edit interfaces ce1-fpc/pic/port]** hierarchy level, as shown in the following example:

```
[edit interfaces]
user@host# show
ce1-0/0/1 {
  partition 1 timeslots 1-4 interface-type ds;
}
```

After you partition the DS interface, configure the CESoPSN options on it. See [“Setting the CESoPSN Options” on page 51](#).

To configure CE1 channels down to a DS interface:

1. Create the CE1 interface.

```
[edit interfaces]
user@host# edit interfaces ce1-fpc/pic/port
```

For example:

```
[edit interfaces]
user@host# edit interface ce1-0/0/1
```

2. Configure the partition, the time slot, and the interface type.

```
[edit interfaces ce1-fpc/pic/port]
user@host# set partition partition-number timeslots timeslots interface-type ds;
```

For example:

```
[edit interfaces ce1-0/0/1]
user@host# set partition 1 timeslots 1-4 interface-type ds;
```



**NOTE:** You can assign multiple time slots on a CE1 interface; in the configuration, separate the time slots by comma without spaces. For example:

```
[edit interfaces ce1-0/0/1]
user@host# set partition 1 timeslots 1-4,9,22-31 interface-type ds;
```

3. Configure the CESoPSN encapsulation for the DS interface.

```
[edit interfaces ds-fpc/pic/port:partition]
user@host# set encapsulation encapsulation-type
```

For example:

```
[edit interfaces ds-0/0/1:1]
user@host# set encapsulation cesopsn
```

4. Configure the logical interface for the DS interface.

```
[edit interfaces ds-fpc/pic/port:partition]
user@host# set unit logical-unit-number;
```

For example:

```
[edit interfaces ds-0/0/1:1]
user@host# set unit 0
```

When you are finished configuring CE1 channels down to a DS interface, enter the **commit** command from configuration mode.

From configuration mode, confirm your configuration by entering the **show** command.

For example:

```
[edit interfaces]
user@host# show
ce1-0/0/1 {
  partition 1 timeslots 1-4 interface-type ds;
}
ds-0/0/1:1 {
  encapsulation cesopsn;
  unit 0;
}
```

- Related Documentation**
- [Understanding Mobile Backhaul on page 13](#)
  - [Configuring CESoPSN Encapsulation on DS Interfaces on page 62](#)

## Configuring CESoPSN on Channelized E1/T1 Circuit Emulation MIC on ACX Series

This configuration applies to the mobile backhaul application shown in [Figure 3 on page 14](#).

- [Configuring T1/E1 Framing Mode at the MIC Level on page 68](#)
- [Configuring CT1 Interface Down to DS channels on page 68](#)
- [Configuring CESoPSN on DS Interfaces on page 69](#)

### Configuring T1/E1 Framing Mode at the MIC Level

To set the framing mode at the MIC (ACX-MIC-16CHE1-T1-CE) level, for all four ports on the MIC, include the **framing** statement at the **[edit chassis fpc slot pic slot]** hierarchy level.

```
[edit chassis fpc slot pic slot]
user@host# set framing (t1 | e1);
```

After a MIC is brought online, interfaces are created for the MIC's available ports on the basis of the MIC type and the framing option used.

- If you include the **framing t1** statement, 16 CT1 interfaces are created.
- If you include the **framing e1** statement, 16 CE1 interfaces are created.



**NOTE:** If you set the **framing** option incorrectly for the MIC type, the commit operation fails.

Bit error rate test (BERT) patterns with all binary 1s (ones) received by CT1/CE1 interfaces on Circuit Emulation MICs configured for CESoPSN do not result in an alarm indication signal (AIS) defect. As a result, the CT1/CE1 interfaces remain up.

### Configuring CT1 Interface Down to DS channels

To configure a channelized T1 (CT1) interface down to DS channels, include the **partition** statement at the **[edit interfaces ct1-mpc-slot/mic-slot/port-number]** hierarchy level:



**NOTE:** To configure a CE1 interface down to DS channels, replace **ct1** with **ce1** in the following procedure.

1. In configuration mode, go to the **[edit interfaces ct1-mpc-slot/mic-slot/port-number]** hierarchy level.

```
[edit]
user@host# edit interfaces ct1-mpc-slot/mic-slot/port-number
```

For example:

```
[edit]
user@host# edit interfaces ct1-1/0/0
```

2. Configure the sublevel interface partition index and the time slots, and set the interface type as **ds**.

```
[edit interfaces ct1-mpc-slot/mic-slot/port-number]
user@host# set partition partition-number timeslots timeslots interface-type ds
```

For example:

```
[edit interfaces ct1-1/0/0]
user@host# set partition 1 timeslots 1-4 interface-type ds
```



**NOTE:** You can assign multiple time slots on a CT1 interface. In the **set** command, separate the time slots by commas and do not include spaces between them. For example:

```
[edit interfaces ct1-1/0/0]
user@host# set partition 1 timeslots 1-4,9,22-24 interface-type ds
```

To verify this configuration, use the **show** command at the **[edit interfaces ct1-1/0/0]** hierarchy level.

```
[edit interfaces ct1-1/0/0]
user@host# show
partition 1 timeslots 1-4 interface-type ds;
```

An NxDS0 interface can be configured from a CT1 interface. Here *N* represents the number of time slots on the CT1 interface. The value of *N* is:

- 1 through 24 when a DS0 interface is configured from a CT1 interface.
- 1 through 31 when a DS0 interface is configured from a CE1 interface.

After you partition the DS interface, configure CESoPSN options on it. See [“Setting the CESoPSN Options” on page 51](#).

## Configuring CESoPSN on DS Interfaces

To configure CESoPSN encapsulation on a DS interface, include the **encapsulation** statement at the **[edit interfaces ds-mpc-slot/mic-slot/port-number:channel]** hierarchy level.

1. In configuration mode, go to the **[edit interfaces ds-mpc-slot/mic-slot/port-number:channel]** hierarchy level.

```
[edit]
user@host# edit interfaces ds-mpc-slot/mic-slot/ port-number:channel
```

For example:

```
[edit]
```

```
user@host# edit interfaces ds-1/0/0:1
```

2. Configure CESoPSN as the encapsulation type.

```
[edit interfaces ds-mpc-slot/mic-slot/port-number:partition ]
user@host# set encapsulation cesopsn
```

For example:

```
[edit interfaces ds-1/0/0:1 ]
user@host# set encapsulation cesopsn
```

3. Configure the logical interface for the DS interface.

```
[edit interfaces ds-mpc-slot/mic-slot/port-number:partition ]
uset@host# set unit interface-unit-number
```

For example:

```
[edit interfaces ds-1/0/0:1 ]
user@host# set unit 0
```

To verify this configuration, use the **show** command at the **[edit interfaces ds-1/0/0:1]** hierarchy level.

```
[edit interfaces ds-1/0/0:1]
user@host# show
encapsulation cesopsn;
unit 0;
```

#### Related Documentation

- [16-Port Channelized E1/T1 Circuit Emulation MIC Overview](#)

## CHAPTER 6

# Configuring ATM Support on Circuit Emulation PICs

- [ATM Support on Circuit Emulation PICs Overview on page 71](#)
- [Configuring the 4-Port Channelized COC3/STM1 Circuit Emulation PIC on page 74](#)
- [Configuring the 12-Port Channelized T1/E1 Circuit Emulation PIC on page 76](#)
- [Understanding Inverse Multiplexing for ATM on page 80](#)
- [ATM IMA Configuration Overview on page 84](#)
- [Configuring ATM IMA on page 91](#)
- [Configuring ATM Pseudowires on page 94](#)
- [Configuring ATM Cell-Relay Pseudowire on page 96](#)
- [ATM Cell Relay Pseudowire VPI/VCI Swapping Overview on page 100](#)
- [Configuring ATM Cell-Relay Pseudowire VPI/VCI Swapping on page 101](#)
- [Configuring Layer 2 Circuit and Layer 2 VPN Pseudowires on page 107](#)
- [Configuring EPD Threshold on page 107](#)
- [Configuring ATM QoS or Shaping on page 108](#)

## ATM Support on Circuit Emulation PICs Overview

---

The following components support ATM over MPLS (RFC 4717) and packet encapsulations (RFC 2684):

- 4-port COC3/CSTM1 Circuit Emulation PIC on M7i and M10i routers.
- 12-port T1/E1 Circuit Emulation PIC on M7i and M10i routers.
- Channelized OC3/STM1 (Multi-Rate) Circuit Emulation MIC with SFP (MIC-3D-4COC3-1COC12-CE) on MX Series routers.
- 16-Port Channelized E1/T1 Circuit Emulation MIC (MIC-3D-16CHE1-T1-CE) on MX Series routers.

Circuit Emulation PIC ATM configuration and behavior is consistent with existing ATM2 PICs.



**NOTE:** Circuit Emulation PICs require firmware version `rom-ce-9.3.pbin` or `rom-ce-10.0.pbin` for ATM IMA functionality on M7i, M10i, M40e, M120, and M320 routers running JUNOS OS Release 10.0R1 or later.

- [ATM OAM Support on page 72](#)
- [Protocol and Encapsulation Support on page 72](#)
- [Scaling Support on page 73](#)
- [Limitations to ATM Support on Circuit Emulation PICs on page 73](#)

## ATM OAM Support

ATM OAM supports:

- Generation and monitoring of F4 and F5 OAM cells types:
  - F4 AIS (end-to-end)
  - F4 RDI (end-to-end)
  - F4 loopback (end-to-end)
  - F5 loopback
  - F5 AIS
  - F5 RDI
- Generation and monitoring of end-to-end cells of type AIS and RDI
- Monitor and terminate loopback cells
- OAM on each VP and VC simultaneously

VP Pseudowires (CCC Encapsulation)—In the case of ATM virtual path (VP) pseudowires—all virtual circuits (VCs) in a VP are transported over a single *N-to-one* mode pseudowire—all F4 and F5 OAM cells are forwarded through the pseudowire.

Port Pseudowires (CCC Encapsulation)—Like VP pseudowires, with port pseudowires, all F4 and F5 OAM cells are forwarded through the pseudowire.

VC Pseudowires (CCC Encapsulation)—In the case of VC pseudowires, F5 OAM cells are forwarded through the pseudowire, while F4 OAM cells are terminated at the Routing Engine.

## Protocol and Encapsulation Support

The following protocols are supported:

- QoS or CoS queues. All virtual circuit (VCs) are unspecified bit rate (UBR).



**NOTE:** This protocol is not supported on M7i and M10i routers.

- ATM over MPLS (RFC 4717)
- ATM via dynamic labels (LDP, RSVP-TE)

NxDSO grooming is not supported

The following ATM2 encapsulations are not supported:

- **atm-cisco-nlpid**—Cisco-compatible ATM NLPID encapsulation
- **atm-mlppp-llc**—ATM MLPPP over AAL5/LLC
- **atm-nlpid**—ATM NLPID encapsulation
- **atm-ppp-llc**—ATM PPP over AAL5/LLC
- **atm-ppp-vc-mux**—ATM PPP over raw AAL5
- **atm-snap**—ATM LLC/SNAP encapsulation
- **atm-tcc-snap**—ATM LLC/SNAP for translational cross-connect
- **atm-tcc-vc-mux**—ATM VC for translational cross-connect
- **vlan-vci-ccc**—CCC for VLAN Q-in-Q and ATM VPI/VCI interworking
- **atm-vc-mux**—ATM VC multiplexing
- **ether-over-atm-llc**—Ethernet over ATM (LLC/SNAP) encapsulation
- **ether-vpls-over-atm-llc**—Ethernet VPLS over ATM (bridging) encapsulation

## Scaling Support

[Table 4 on page 73](#) lists the maximum number of virtual circuits (VCs) that are supported on various components on the M10i router, on the M7i router, and on MX Series routers.

*Table 4: Maximum Number of VCs*

Component	Maximum Number of VCs
12-port Channelized T1/E1 Circuit Emulation PIC	1000 VCs
4-port Channelized COC3/STM1 Circuit Emulation PIC	2000 VCs
Channelized OC3/STM1 (Multi-Rate) Circuit Emulation MIC with SFP	2000 VCs
16-Port Channelized E1/T1 Circuit Emulation MIC	1000 VCs

## Limitations to ATM Support on Circuit Emulation PICs

The following limitations apply to ATM support on Circuit Emulation PICs:

- **Packet MTU**—Packet MTU is limited to 2048 bytes.
- **Trunk mode ATM pseudowires**—Circuit Emulation PICs do not support trunk mode ATM pseudowires.

- OAM-FM segment—Segment F4 flows are not supported. Only end-to-end F4 flows are supported.
- IP and Ethernet encapsulations—IP and Ethernet encapsulations are not supported.
- F5 OAM—OAM termination is not supported.

**Related Documentation**

- [Configuring the 12-Port Channelized T1/E1 Circuit Emulation PIC on page 76](#)
- [Configuring the 4-Port Channelized COC3/STM1 Circuit Emulation PIC on page 74](#)
- [ATM IMA Configuration Overview on page 84](#)
- [Configuring ATM IMA on page 91](#)
- [Configuring ATM Pseudowires on page 94](#)
- [Configuring EPD Threshold on page 107](#)
- [Configuring Layer 2 Circuit and Layer 2 VPN Pseudowires on page 107](#)

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## Configuring the 4-Port Channelized COC3/STM1 Circuit Emulation PIC

---

- [T1/E1 Mode Selection on page 74](#)
- [Configuring a Port for SONET or SDH Mode on a 4-Port Channelized COC3/STM1 Circuit Emulation PIC on page 75](#)
- [Configuring an ATM Interface on a Channelized OC1 interface on page 76](#)

### T1/E1 Mode Selection

All ATM interfaces are either T1 or E1 channels within the COC3/CSTM1 hierarchy. Each COC3 interface can be partitioned as 3 COC1 slices, each of which in turn can be partitioned further into 28 ATM interfaces and the size of each interface created is that of a T1. Each CS1 can be portioned as 1 CAU4, which can be further partitioned as E1 sized ATM interfaces.

To configure the T1/E1 mode selection, note the following:

1. To create **coc3-fpc/pic/port** or **cstm1-fpc/pic/port** interfaces, chassisd will look for configuration at the **[edit chassis fpc fpc-slot pic pic-slot port port framing (sonet | sdh)]** hierarchy level. If the **sdh** option is specified, chassisd will create a **cstm1-fpc/pic/port** interface. Otherwise, chassisd will create **coc3-fpc/pic/port** interfaces.
2. Only interface **coc1** can be created from **coc3**, and **t1** can be created from **coc1**.
3. Only interface **cau4** can be created from **cstm1**, and **e1** can be created from **cau4**.

[Figure 7 on page 75](#) and [Figure 8 on page 75](#) illustrate the possible interfaces that can be created on the 4-port Channelized COC3/STM1 Circuit Emulation PIC.

Figure 7: 4-Port Channelized COC3/STM1 Circuit Emulation PIC Possible Interfaces (T1 Size)

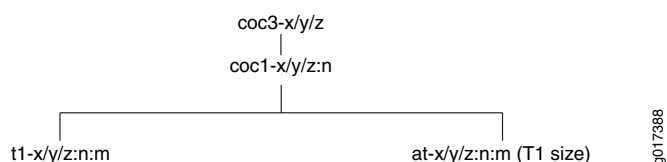
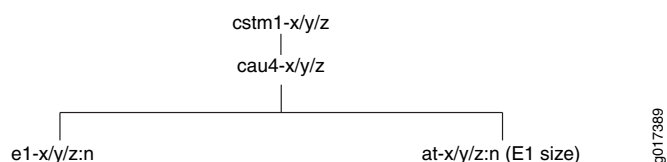


Figure 8: 4-Port Channelized COC3/STM1 Circuit Emulation PIC Possible Interfaces (E1 Size)



Subrate T1 is not supported.

ATM NxDS0 grooming is not supported.

External and internal loopback of T1/E1 (on ct1/ce1 physical interfaces) can be configured using the **sonet-options** statement. By default, no loopback is configured.

## Configuring a Port for SONET or SDH Mode on a 4-Port Channelized COC3/STM1 Circuit Emulation PIC

Each port of the 4-port Channelized COC3/STM1 Circuit Emulation PIC can be independently configured for either SONET or SDH mode. To configure a port for either SONET or SDH mode, enter the **framing (sonet | sdh)** statement at the **[chassis fpc number pic number port number]** hierarchy level.

The following example shows how to configure FPC 1, PIC 1, and port 0 for SONET mode and port 1 for SDH mode:

```

set chassis fpc 1 pic 1 port 0 framing sonet
set chassis fpc 1 pic 1 port 1 framing sdh
  
```

Or specify the following:

```

[edit]
fpc 1 {
  pic 1 {
    port 0 {
      framing sonet;
    }
    port 1 {
      framing sdh;
    }
  }
}
  
```

## Configuring an ATM Interface on a Channelized OC1 interface

To create an ATM interface on a channelized OC1 interface (COC1), enter the following command:

To create an ATM interface on CAU4, enter the following command:

**set interfaces cau4-fpc/pic/port partition interface-type at**

Or specify the following:

```
interfaces {
  cau4-fpc/pic/port {
  }
}
```

You can use the **show chassis hardware** command to display a list of the installed PICs.

### Related Documentation

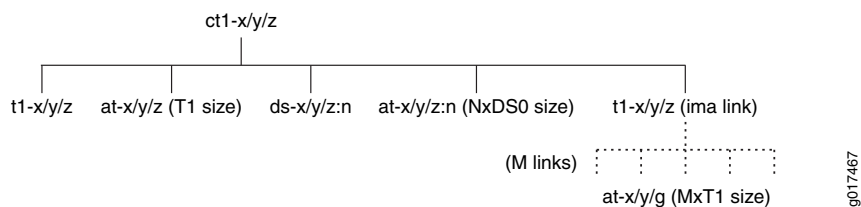
- [ATM Support on Circuit Emulation PICs Overview on page 71](#)

## Configuring the 12-Port Channelized T1/E1 Circuit Emulation PIC

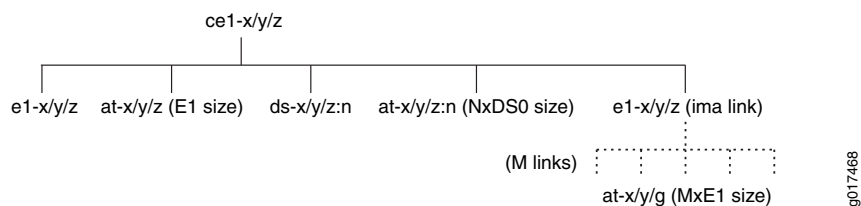
When the 12-port Channelized T1/E1 Circuit Emulation PIC is brought online, 12 channelized T1 (ct1) interfaces or 12 channelized E1 (ce1) interfaces are created, depending on the T1 or E1 mode selection of the PIC.

[Figure 9 on page 76](#) and [Figure 10 on page 76](#) illustrate the possible interfaces that can be created on the 12-port T1/E1 Circuit Emulation PIC.

**Figure 9: 12-Port T1/E1 Circuit Emulation PIC Possible Interfaces (T1 Size)**



**Figure 10: 12-Port T1/E1 Circuit Emulation PIC Possible Interfaces (E1 Size)**



The following sections explain:

- [Configuring CT1/CE1 Interfaces on page 77](#)
- [Configuring Interface-Specific Options on page 78](#)

## Configuring CT1/CE1 Interfaces

The following sections explain how to configure the T1/E1 mode at the PIC level to create ct1 interfaces or ce1 interfaces and to create an ATM interface on a CT1 interface or a CE1 interface:

- [Configuring T1/E1 Mode at the PIC level on page 77](#)
- [Creating an ATM Interface on a CT1 or CE1 on page 77](#)
- [Creating an ATM Interface on a CE1 Interface on page 78](#)

### Configuring T1/E1 Mode at the PIC level

To configure T1/E1 mode at the PIC level:

1. Go to the **[edit chassis]** hierarchy level.

```
[edit]
user@host# edit chassis
```

2. Configure the FPC slot and PIC slot.

```
[edit chassis]
user@host# edit fpc fpc-slot pic pic-slot
```

3. Configure the framing mode as E1 or T1.

```
[edit chassis fpc fpc-slot pic pic-slot]
user@host# edit framing (e1 | t1)
```

After the PIC is brought online, 12 channelized T1 (ct1) interfaces or 12 channelized E1 (ce1) interfaces are created.



**NOTE:** If the mode is not manually configured, then the PIC defaults to T1.

### Creating an ATM Interface on a CT1 or CE1

To create an ATM interface on a CT1 interface:

1. Go to the **[edit interfaces ct1-fpc/pic/port]** hierarchy level.

```
[edit]
user@host# edit interfaces ct1-fpc/pic/port
```

2. Configure the **no-partition** statement to use channelized interface as clear channel and to set the interface type as an ATM interface.

```
[edit interfaces ct1-fpc/pic/port]
user@host# set no-partition interface-type at
```

The interface **at-fpc/pic/port** is created.

You can use the **show chassis hardware** command to display a list of the installed PICs.

### Creating an ATM Interface on a CE1 Interface

---

To create an ATM interface on a CE1 interface, enter the following command:

1. Go to the **[edit interfaces ce1-fpc/pic/port]** hierarchy level.

```
[edit]
user@host# [edit interfaces ce1-fpc/pic/port]
set interfaces no-partition interface-type at
```

2. Configure the **no-partition** statement to use channelized interface as clear channel and to set the interface type as an ATM interface.

```
[edit interfaces ce1-fpc/pic/port]
user@host# set no-partition interface-type at
```

The interface **at-fpc/pic/port** is created.

You can use the **show chassis hardware** command to display a list of the installed PICs.

## Configuring Interface-Specific Options

ATM supports T1 and E1 interfaces on 12-port Channelized T1/E1 Circuit Emulation PICs. The following sections explain how to configure the interface-specific options for ATM, E1, and T1 interfaces.

- [Configuring ATM Interface-Specific Options on page 78](#)
- [Configuring E1 Interface-Specific Options on page 79](#)
- [Configuring T1 Interface-Specific Options on page 79](#)

### Configuring ATM Interface-Specific Options

---

To configure ATM interface-specific options:

1. Go to the **[edit interfaces at-fpc-slot/pic-slot/port:logical-unit]**.

```
[edit]
user@host# edit interfaces at-fpc-slot/pic-slot/port:logical-unit
```

2. Configure the supported PIC type as a CE PIC—**atm-ce**, a ATM I PIC—**atm1**, or as a ATM II IQ PIC—**atm2** under the **atm-options** statement.

```
[edit interfaces at-fpc-slot/pic-slot/port:logical-unit]
user@host# edit atm-options (atm-ce | atm1 | atm2)
```









## Supported Platforms

The following are the various Juniper Networks routers and their components that support inverse multiplexing for ATM (IMA):

- 16-port Channelized E1/T1 Circuit Emulation MIC (MIC-3D-16CHE1-T1-CE) on MX Series routers (from Junos OS Release 13.2R1 onward).
- 4-port Channelized OC3/STM1 (Multi-Rate) Circuit Emulation MIC with SFP (MIC-3D-4COC3-1COC12-CE) on MX Series routers (from Junos OS Release 13.2R1 onward).
- 4-port Channelized OC3/STM1 Circuit Emulation PIC with SFP (PB-4CHOC3-CE-SFP) on M7i, M10i, M40e, M120, and M320 routers supports channelized OC3/STM1 (down to T1) ATM IMA.
- 12-port E1/T1 Circuit Emulation PIC (PB-12T1E1-CE-TELCO) on M7i, M10i, M40e, M120, and M320 routers supports discrete T1 ATM IMA.



**NOTE:** Circuit Emulation PICs require firmware version `rom-ce-9.3.pbin` or `rom-ce-10.0.pbin` for ATM IMA functionality on M7i, M10i, M40e, M120, and M320 routers running Junos OS Release 10.0R1 or later.

### Related Documentation

- [ATM IMA Configuration Overview on page 84](#)
- [ATM Support on Circuit Emulation PICs Overview on page 71](#)
- [Configuring ATM IMA on page 91](#)





















































```
        encapsulation atm-ccc-cell-relay;
        vci 0.100;
    }
    unit 1 {
        encapsulation atm-ccc-cell-relay;
        vci 1.100;
        shaping {
            cbr|rtvbr|vbr {
                <shaping specific parameters> cdvt
            }
        }
    }
}
```

- Related Documentation**
- [ATM Support on Circuit Emulation PICs Overview on page 71](#)
  - *shaping*

## PART 3

# Troubleshooting Information

- [Troubleshooting Circuit Emulation Interfaces on page 113](#)



















## PART 4

# Configuration Statements and Operational Commands

- Configuration Statements on page 123
- Operational Commands on page 135



## CHAPTER 8

# Configuration Statements

- [cesopsn-options on page 124](#)
- [event \(CFM\) on page 125](#)
- [fast-aps-switch on page 126](#)
- [ima-group-options on page 127](#)
- [ima-link-options on page 129](#)
- [no-vpivci-swapping on page 129](#)
- [payload-size on page 130](#)
- [psn-vci \(ATM CCC Cell-Relay Promiscuous Mode VPI/VCI Swapping\) on page 130](#)
- [psn-vpi \(ATM CCC Cell-Relay Promiscuous Mode VPI/VCI Swapping\) on page 131](#)
- [satop-options on page 132](#)



## event (CFM)

<b>Syntax</b>	<pre> event {   adjacency-loss;   interface-status-tlv [lower-layer-down down];   port-status-tlv blocked;   rdi; } </pre>
<b>Hierarchy Level</b>	[edit protocols oam ethernet connectivity-fault-management action-profile]
<b>Release Information</b>	Statement introduced in Junos OS Release 10.1
<b>Description</b>	Configure threshold values for connectivity fault management events in an action profile.
<b>Options</b>	<p><b>adjacency-loss</b>—Connectivity is lost.</p> <p><b>interface-status-tlv [ lower-layer-down down ]</b>—Values that need to be monitored in interface status TLV.</p> <p><b>port-status-tlv</b>—Values that need to be monitored in port status TLV.</p> <p><b>rdi</b>—RDI received from some MEP.</p>
<b>Required Privilege Level</b>	<p><b>interface</b>—To view this statement in the configuration.</p> <p><b>interface-control</b>—To add this statement to the configuration.</p>
<b>Related Documentation</b>	<ul style="list-style-type: none"> <li>• <i>Configuring a CFM Action Profile to Specify CFM Actions for CFM Events</i></li> <li>• <i>interface-status-tlv</i></li> <li>• <i>port-status-tlv</i></li> </ul>











## psn-vpi (ATM CCC Cell-Relay Promiscuous Mode VPI/VCI Swapping)

<b>Syntax</b>	<code>psn-vpi <i>psn-vpi-identifier</i>;</code>
<b>Hierarchy Level</b>	[edit interfaces at- <i>fpc/pic/port</i> unit <i>logical-unit-number</i> ]
<b>Release Information</b>	Statement introduced in Junos OS Release 12.1. Statement introduced in Junos OS Release 12.2 for the ACX Series Universal Metro Routers.
<b>Description</b>	(MX Series routers) Swap only the VPI values on both egress and ingress in ATM CCC cell-relay mode on ATM MICs. This statement is not compatible with the ATM policing feature.
<b>Options</b>	<i>psn-vpi-identifier</i> —ATM PSN virtual path identifier. <b>Range:</b> 0 through 255
<b>Required Privilege Level</b>	interface—To view this statement in the configuration. interface-control—To add this statement to the configuration.
<b>Related Documentation</b>	<ul style="list-style-type: none"><li>• <i>Configuring ATM Cell-Relay Promiscuous Mode</i></li></ul>



**jitter-buffer-latency** *milliseconds*—Number of milliseconds delay in jitter buffer (from 1 to 1000 milliseconds).

**jitter-buffer-packets** *packets*—Number of packets in jitter buffer (from 1 to 64).

**payload-size** *bytes*—Payload size in integer number of bytes.

<b>Required Privilege Level</b>	interface—To view this statement in the configuration.
	interface-control—To add this statement to the configuration.

<b>Related Documentation</b>	• <a href="#">Configuring SToP on 4-Port Channelized OC3/STM1 Circuit Emulation MICs on page 19</a>
	• <a href="#">Configuring SToP Emulation on T1/E1 Interfaces on 12-Port Channelized T1/E1 Circuit Emulation PICs on page 26</a>
	• <a href="#">ATM Support on Circuit Emulation PICs Overview on page 71</a>



## CHAPTER 9

# Operational Commands

- `show interfaces (ATM)`
- `show interfaces (T1, E1, or DS)`
- `show interfaces extensive`

















Table 13: ATM show interfaces Output Fields (continued)

Field Name	Field Description	Level of Output
Received SDH overhead  Transmitted SDH overhead	<p>Values of the received and transmitted SONET overhead:</p> <ul style="list-style-type: none"> <li>• <b>C2</b>—Signal label. This byte is allocated to identify the construction and content of the STS-level SPE and for PDI-P.</li> <li>• <b>F1</b>—Section user channel byte. This byte is set aside for the purposes of users.</li> <li>• <b>K1</b> and <b>K2</b>—These bytes are allocated for APS signaling for the protection of the multiplex section.</li> <li>• <b>J0</b>—Section trace. This byte is defined for STS-1 number 1 of an STS-<i>N</i> signal. This byte is used to transmit a 1-byte fixed-length string or a 16-byte message so that a receiving terminal in a section can verify its continued connection to the intended transmitter.</li> <li>• <b>S1</b>—Synchronization status. The S1 byte is located in the first STS-1 of an STS-<i>N</i>.</li> <li>• <b>Z3</b> and <b>Z4</b>—These bytes are allocated for future use.</li> </ul>	extensive
Received path trace  Transmitted path trace	<p>SONET/SDH interfaces allow path trace bytes to be sent inband across the SONET/SDH link. Juniper Networks and other router manufacturers use these bytes to help diagnose misconfigurations and network errors by setting the transmitted path trace message so that it contains the system hostname and name of the physical interface. The received path trace value is the message received from the router at the other end of the fiber. The transmitted path trace value is the message that this router transmits.</p>	extensive
ATM Status	<p>ATM state information:</p> <ul style="list-style-type: none"> <li>• <b>HCS State</b>—Status of the header check sequence. ATM uses the HCS field in the cell header in the cell delineation process to frame ATM cell boundaries. The HCS is an FCS-8 calculation over the first four octets of the ATM cell header.</li> <li>• <b>LOC</b>—Current loss of cell (LOC) delineation state. <b>OK</b> means that no LOC is currently asserted.</li> </ul>	extensive



Table 13: ATM show interfaces Output Fields (continued)

Field Name	Field Description	Level of Output
CoS information	<p>Information about the CoS queue for the physical interface.</p> <ul style="list-style-type: none"><li>• <b>CoS transmit queue</b>—Queue number and its associated user-configured forwarding class name.</li><li>• <b>Bandwidth %</b>—Percentage of bandwidth allocated to the queue.</li><li>• <b>Bandwidth bps</b>—Bandwidth allocated to the queue (in bps).</li><li>• <b>Buffer %</b>—Percentage of buffer space allocated to the queue.</li><li>• <b>Buffer usec</b>—Amount of buffer space allocated to the queue, in microseconds. This value is nonzero only if the buffer size is configured in terms of time.</li><li>• <b>Priority</b>—Queue priority: <b>low</b> or <b>high</b>.</li><li>• <b>Limit</b>—Displayed if rate limiting is configured for the queue. Possible values are <b>none</b> and <b>exact</b>. If <b>exact</b> is configured, the queue transmits only up to the configured bandwidth, even if excess bandwidth is available. If <b>none</b> is configured, the queue transmits beyond the configured bandwidth if bandwidth is available.</li></ul>	extensive





Table 13: ATM show interfaces Output Fields (continued)

Field Name	Field Description	Level of Output
<b>Broadcast</b>	Broadcast address.	<b>detail extensive none</b>
<b>Generation</b>	Unique number for use by Juniper Networks technical support only.	<b>detail extensive</b>
<b>VCI</b>	Virtual circuit identifier number and information: <ul style="list-style-type: none"> <li>• <b>Flags</b>—VCI flags:               <ul style="list-style-type: none"> <li>• <b>Active</b>—VCI is up and in working condition.</li> <li>• <b>CCC down</b>—VCI CCC is not in working condition.</li> <li>• <b>Closed</b>—VCI is closed because the user disabled the logical or physical interface from the CLI.</li> <li>• <b>Configured</b>—VCI is configured.</li> <li>• <b>Down</b>—VCI is not in working condition. The VCI might have alarms, defects, F5 AIS/RDI, or no response to OAM loopback cells.</li> <li>• <b>ILMI</b>—VCI is up and in working condition.</li> <li>• <b>OAM</b>—OAM loopback is enabled.</li> <li>• <b>Multicast</b>—VCI is a multicast VCI or DLCI.</li> <li>• <b>Multipoint destination</b>—VCI is configured as a multipoint destination.</li> <li>• <b>None</b>—No VCI flags.</li> <li>• <b>Passive-OAM</b>—Passive OAM is enabled.</li> <li>• <b>Shaping</b>—Shaping is enabled.</li> <li>• <b>Sustained</b>—Shaping rate is set to <b>Sustained</b>.</li> <li>• <b>Unconfigured</b>—VCI is not configured.</li> </ul> </li> <li>• <b>Total down time</b>—Total number of seconds the VCI has been down, using the format <b>Total down time: hh:mm:ss</b> or <b>Never</b>.</li> <li>• <b>Last down</b>—Time of last <b>Down</b> transition, using the format <b>Last down: hh:mm:ss</b>.</li> <li>• <b>EPD threshold</b>—(ATM2 only) Threshold at which a packet is dropped when the queue size (in number of cells) exceeds the early packet-discard (EPD) value.</li> </ul>	All levels



























































































```
CE AIS dropped          0
CE Dropped              0          0
CE Overrun Events       0
CE Underrun Events      1
Protocol ccc, MTU: 1504, Generation: 857, Route table: 0
  Flags: Is-Primary
```

## show interfaces extensive

---

**Syntax**    show interfaces extensive

**Release Information**    Command introduced before Junos OS Release 7.4.  
Command introduced in Junos OS Release 12.1x48 for PTX Series Packet Transport Routers.  
Command introduced in Junos OS Release 17.2 for PT1000 and PTX10008 Packet Transport Routers.

**Description**    Display extensive information about all interfaces configured on the router.



## NOTE:

- At some times, the cumulative byte counters displayed with the `show interfaces extensive` command on the 10-Gigabit Ethernet MPC with SFP+ is not always increasing and cumulative and does not give the correct results. There is a time lag in collecting these statistics, during which the display might decrease or go from a nonzero number to zero. Eventually, the counter will display the correct result.
- When the `show interfaces extensive` command is executed on a router with an MPC or a T4000 Type 5 FPC, the Input packet rejects counter of the Filter statistics field also displays statistics related to the following packet errors:
  - Invalid VLAN range
  - Tagged packet received on an untagged interface
- When the `show interfaces extensive` command is executed on an interface that is configured on a T4000 Type 5 FPC, the IPv6 transit statistics field displays:
  - Total statistics (sum of transit and local statistics) at the physical interface level
  - Transit statistics at the logical interface level
- When the `show interfaces extensive` command is executed on an aggregate interface in a T1600 Core Router, the IPv6 Input bytes is displayed for an aggregate interface. However, the IPv6 Input bytes is always zero on a member link of an aggregated bundle even when there is IPv6 transit traffic on the member link. This is because the logical interface index of the aggregate logical interface is updated but not the logical interface of the member links in the channel lookup table.
- The Output packets field under the Traffic statistics section in the output of the `show interfaces extensive` command includes both IPv4 and IPv6 packets. For example, in a scenario in which both IPv4 and IPv6 packets are being mirrored on the same interface and when you deactivate an IPv4 port-mirroring instance on the chassis, the output of the `show interfaces extensive` command shows a value in the Output packets field of the Traffic statistics section, which is the value of IPv6 packets that are mirrored and not of the IPv4 packets. This behavior is expected.
- For IQ2 PIC interfaces, the output of the `show interfaces extensive` command displays byte statistics that includes Layer 2 headers.
- If there are active OTN defects when an ISSU is performed, and the defect persists after the upgrade completes, the OTN alarm count is incremented by 1. For example, if an OTN alarm is active with a count of 1 and the defect remains after ISSU, the alarm count is incremented to 2. This behavior is expected.





```

MAC statistics:
  Total octets          24721      Transmit 105982
  Total packets        348         1349
  Unicast packets      347         430
  Broadcast packets    1          37
  Multicast packets    0          882
  CRC/Align errors     0          0
  FIFO errors          0          0
  MAC control frames   0          0
  MAC pause frames     0          0
  Oversized frames     0
  Jabber frames        0
  Fragment frames      0
  VLAN tagged frames   0
  Code violations      0

Filter statistics:
  Input packet count    348
  Input packet rejects  0
  Input DA rejects      0
  Input SA rejects      0
  Output packet count   1349
  Output packet pad count 0
  Output packet error count 0
  CAM destination filters: 3, CAM source filters: 0

Autonegotiation information:
  Negotiation status: Complete
  Link partner:
    Link mode: Full-duplex, Flow control: None, Remote fault: OK

Packet Forwarding Engine configuration:
  Destination slot: 0

CoS information:
  CoS transmit queue    Bandwidth    Buffer    Priority    Limit

                                %      bps    %    usec
  0 best-effort          95      95000000 95    0          low    none
  3 network-control      5       5000000 5     0          low    none

Logical interface fe-0/2/0.0 (Index 66) (SNMP ifIndex 46) (Generation 133)
Flags: SNMP-Traps Encapsulation: ENET2
Protocol inet, MTU: 1500, Generation: 142, Route table: 0
Flags: DCU, SCU-out

Destination class          Packets          Bytes
                          (packet-per-second)  (bits-per-second)
  silv1_new                0                0
  (                        0) (                0)
  silv2_new                0                0
  (                        0) (                0)
  silv_misc                0                0
  (                        0) (                0)
  silver0                  0                0
  (                        0) (                0)
  silver2                  0                0
  (                        0) (                0)
  silver3                  0                0
  (                        0) (                0)
  silver4                  0                0
  (                        0) (                0)
  silver5                  0                0
  (                        0) (                0)
  silver6                  0                0
  (                        0) (                0)
  silver7                  0                0

```

```

                                (          0) (          0)
                                0          0
                                (          0) (          0)
                                0          0
Source class                    (packet-per-second) (bits-per-second)
                                0          0
                                (          0) (          0)
                                0          0
                                16600      1062400
                                (          0) (          0)
                                0          0
                                (          0) (          0)
Addresses, Flags: Is-Preferred Is-Primary
Destination: 192.168.220.24/30, Local: 192.168.220.26, Broadcast:
192.168.220.27, Generation: 150

```

### show interfaces extensive (Gigabit Ethernet)

```

user@host> show interfaces ge-5/0/0.0 extensive

Logical interface ge-5/0/0.0 (Index 71) (SNMP ifIndex 1930) (Generation 139)
Flags: SNMP-Traps 0x4000 Encapsulation: ENET2
Traffic statistics:
Input bytes : 0
Output bytes : 42
Input packets: 0
Output packets: 1
Local statistics:
Input bytes : 0
Output bytes : 42
Input packets: 0
Output packets: 1
Transit statistics:
Input bytes : 0 0 bps
Output bytes : 0 0 bps
Input packets: 0 0 pps
Output packets: 0 0 pps
Output Filters: f-any
Protocol inet, MTU: 1500, Generation: 155, Route table: 0
Output Filters: f-inet,
Addresses, Flags: Is-Preferred Is-Primary
Destination: 192.168.220.24/30, Local: 192.168.220.26, Broadcast:
192.168.220.27,
Generation: 170
Protocol multiservice, MTU: Unlimited, Generation: 156, Route table: 0
Flags: Is-Primary
Policer: Input: __default_arp_policer__

```

### show interfaces extensive (10-Gigabit Ethernet)

```

user@host> show interfaces xe-2/1/0 extensive

Physical interface: xe-2/1/0, Enabled, Physical link is Up
Interface index: 258, SNMP ifIndex: 762, Generation: 2046
Link-level type: Ethernet, MTU: 1514, LAN-PHY mode, Speed: 10Gbps, BPDU Error:
None, Loopback: None, Source filtering: Disabled,
Flow control: Enabled
Device flags : Present Running
Interface flags: SNMP-Traps Internal: 0x4000
Link flags : None
CoS queues : 8 supported, 8 maximum usable queues

```

```

Hold-times      : Up 0 ms, Down 0 ms
Current address: 00:00:5E:00:53:00, Hardware address: 00:00:5E:00:53:00
Last flapped   : 2011-12-17 00:19:02 PST (07:36:37 ago)
Statistics last cleared: 2011-12-17 07:55:24 PST (00:00:15 ago)
Traffic statistics:
  Input bytes :          110000          0 bps
  Output bytes :           0          0 bps
  Input packets:          1000          0 pps
  Output packets:           0          0 pps
IPv6 transit statistics:
  Input bytes :          110000
  Output bytes :           0
  Input packets:          1000
  Output packets:           0
Input errors:
  Errors: 0, Drops: 0, Framing errors: 0, Runt: 0, Policed discards: 0, L3
incompletes: 0, L2 channel errors: 0,
  L2 mismatch timeouts: 0, FIFO errors: 0, Resource errors: 0
Output errors:
  Carrier transitions: 0, Errors: 0, Drops: 0, Collisions: 0, Aged packets: 0,
FIFO errors: 0, HS link CRC errors: 0,
  MTU errors: 0, Resource errors: 0
Egress queues: 8 supported, 4 in use
Queue counters:      Queued packets    Transmitted packets    Dropped packets

  0 best-effort          0              0              0

  1 expedited-fo          0              0              0

  2 assured-forw          0              0              0

  3 network-cont          0              0              0

Queue number:      Mapped forwarding classes
  0                best-effort
  1                expedited-forwarding
  2                assured-forwarding
  3                network-control
Active alarms : None
Active defects : None
PCS statistics      Seconds
  Bit errors        0
  Errored blocks    0
MAC statistics:      Receive      Transmit
  Total octets      128000          0
  Total packets     1000            0
  Unicast packets   1000            0
  Broadcast packets 0                0
  Multicast packets 0                0
  CRC/Align errors  0                0
  FIFO errors       0                0
  MAC control frames 0                0
  MAC pause frames  0                0
  Oversized frames  0
  Jabber frames     0
  Fragment frames   0
  VLAN tagged frames 0
  Code violations    0
Filter statistics:
  Input packet count      1000
  Input packet rejects    0

```









































































```

    ge-3/2/5.0  Partner      127  00:24:dc:98:67:c0      127      1      1
    ge-3/3/9.0  Actor       100  00:00:00:00:00:01      127      2
1   ge-3/3/9.0  Partner      127  00:24:dc:98:67:c0      127      2      1

LACP Statistics:      LACP Rx      LACP Tx      Unknown Rx      Illegal Rx
ge-3/2/5.0           38          137           0              0
ge-3/3/9.0           36          139           0              0
Marker Statistics:   Marker Rx      Resp Tx      Unknown Rx      Illegal Rx
ge-3/2/5.0           0            0            0              0
ge-3/3/9.0           0            0            0              0
Protocol inet, MTU: 1500, Generation: 169, Route table: 0
Flags: Sendbcst-pkt-to-re
Addresses, Flags: Is-Preferred Is-Primary
Destination: 1.1.1/24, Local: 1.1.1.2, Broadcast: 1.1.1.255, Generation:
153 Protocol multiservice, MTU: Unlimited, Generation: 170, Route table: 0
Flags: Is-Primary
Policer: Input: __default_arp_policer__

```

