Configuring Channelized T3 IQ Interfaces

This section describes how to configure channelized T3 intelligent queuing (IQ) interfaces, discussing the following topics:

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Configuring T3 IQ Interfaces

To configure a T3 interface, include the no-partition and interface-type statements at the [edit interfaces ct3-*fpc/pic/port*] hierarchy level:

```
[edit interfaces ct3-fpc/pic/port]
[Unresolved xref] interface-type t3;
```

This configuration creates interface t3-*fpc/pic/port*.

Configuring T1 IQ Interfaces

On a Channelized DS3 IQ or IQE Physical Interface Card (PIC), you can create up to 112 T1 interfaces. To configure a T1 interface on a Channelized DS3 IQ or IQE PIC, include the partition and interface-type statements at the [edit interfaces ct3-*fpc/pic/port*] hierarchy level, specifying the t1 interface type:

```
[edit interfaces ct3-fpc/pic/port]
partition partition-number interface-type t1;
```

This configuration creates interface t1-*fpc/pic/port*:*channel*.

The partition number is the sublevel interface partition index and is correlated with the channel number. For channelized T3 interfaces, the partition number can be from 1 through 28.

NOTE: For channelized T3 interfaces, channel numbering begins with 0 (:0). For channelized T3 IQ and IQE interfaces, channel numbering begins with 1 (:1).

The interface type is the channelized interface type or clear channel you are creating. For channelized T3 interfaces, *type* can be ct1 or t1.

Example: Configuring T1 IQ and IQE Interfaces

Configure the following five T1 interfaces:

t1-0/0/0:1

t1-0/0/0:2
t1-0/0/0:3
t1-0/0/0:4
t1-0/0/0:5

```
[edit interfaces ct3-0/0/0]
partition 1-5 interface-type t1;
```

For a full configuration example, see the *JUNOS Feature Guide*.

Configuring Fractional T1 IQ and IQE Interfaces

By default, all the time slots on a channelized T1 interface are used. To configure a fractional T1 interface on a Channelized DS3 IQ or IQE PIC, perform the following tasks:

1. Configure a T1 IQ interface. For more information, see "Configuring T1 IQ Interfaces" on page 1.

   This configuration creates interface t1-*fpc/pic/port*:*channel*.

2. Configure the number of time slots allocated to the T1 IQ interface by including the timeslots statement at the [edit interfaces t1-*fpc/pic/port*:*channel* t1-options] hierarchy level:

   ```
   [edit interfaces t1-fpc/pic/port t1-options]
   timeslots time-slot-range;
   ```

For channelized T1 IQ interfaces, the time-slot range is from 1 through 24. You can designate any combination of time slots. To configure ranges, use hyphens. To configure discontinuous time slots, use commas. Do not include spaces. For more information about T1 time slots, see Configuring Fractional T1 Time Slots.

Example: Configuring Fractional T1 IQ Interfaces

Configure a fractional T1 interface that uses time slots 1 through 10:

```
[edit interfaces ct3-0/0/0:1]
partition 1 interface-type t1;
[edit interfaces t1-0/0/0:1:1 t1-options]
timeslots 1-10;
```

For a full configuration example, see the *JUNOS Feature Guide*.

Configuring an NxDS0 IQ Interface

By default, all the time slots on a channelized T3 interface are used. To configure an *N*xDS0 IQ interface on a Channelized DS3 IQ or IQE PIC, perform the following tasks:

1. Partition the channelized T3 interface into channelized T1 interfaces by including the partition and interface-type statements at the [edit interfaces ct3-*fpc/pic/port*] hierarchy level, specifying the ct1 interface type:

[edit interfaces ct3-*fpc/pic/port*]
partition *partition-number* interface-type ct1;

This configuration creates interface ct1-*fpc/pic/port*:*channel*.

The partition number is the sublevel interface partition index and is correlated with the channel number. For channelized T1 interfaces, the partition number can be from 1 through 28.

The interface type is the channelized interface type or clear channel you are creating. For channelized T3 interfaces, *type* can be ct1 or t1.

NOTE: For channelized T3 interfaces, channel numbering begins with 0 (:0). For channelized T3 IQ interfaces, channel numbering begins with 1 (:1).

2. Configure the number of time slots allocated to the *N*xDS0 IQ interface by including the partition, timeslots, and interface-type statements at the [edit interfaces ct1-*fpc/pic/port*:*channel*] hierarchy level, specifying the ds interface type:

   [edit interfaces ct1-*fpc/pic/port*:*channel*]
   partition *partition-number* timeslots *time-slot-range* interface-type ds;

For channelized T1 IQ interfaces, the partition number range is from 1 through 28; the time-slot range is from 1 through 24. You can designate any combination of time slots. To configure ranges, use hyphens. To configure discontinuous time slots, use commas. Do not include spaces. For more information about T1 time slots, see Configuring Fractional T1 Time Slots.

Example: Configuring an NxDS0 IQ Interface

Configure the following two *N*xDS0 interfaces with 10 time slots and 4 time slots, respectively:

ds-0/0/0:1:1
ds-0/0/0:1:2

```
[edit interfaces ct3-0/0/0]
partition 1 interface-type ct1;
[edit interfaces ct1-0/0/0:1]
partition 1 timeslots 1-10 interface-type ds;
partition 2 timeslots 12-16 interface-type ds;
```

For a full configuration example, see the *JUNOS Feature Guide*.