

Configuring the Serial DTR Circuit

A balanced circuit has two currents that are equal in magnitude and opposite in phase. An unbalanced circuit has one current and a ground; if a pair of terminals is unbalanced, one side is connected to electrical ground and the other carries the signal. By default, the DTR circuit is balanced.

For EIA-530 and V.35 interfaces, configure the DTR circuit by including the `dtr-circuit` statement:

```
dtr-circuit (balanced | unbalanced);
```

You can include the `dtr-circuit` statement at the following hierarchy levels:

- [edit interfaces *se-pim/0/port* serial-options]
- [edit interfaces *se-fpc/pic/port* serial-options]

