

Configuring T1 CRC Error Major Alarm Thresholds

JUNOS software collects CRC errors from PICs every second. On Channelized OC3 IQ and IQE PICs, Channelized OC12 IQ and IQE PICs, and Channelized T3 IQ PICs, you can configure major error thresholds for T1 CRC errors.

When the threshold is exceeded for 1 second, a defect condition is declared. If the defect condition continues for the monitoring period, an alarm condition is declared. You can display the CRC error threshold configuration, CRC errors count, and the alarm condition using the `show interfaces extensive` command.

To configure a CRC major error threshold, include the `crc-major-alarm-threshold` statement at the `[edit interfaces interface-name t1-options]` hierarchy level, specifying the errors per bits as `1e-3`, `5e-4`, `1e-4`, `5e-5` or `1e-5`:

```
[edit interfaces interface-name t1-options]
  crc-major-alarm-threshold (1e-3 | 5e-4 | 1e-4 | 5e-5 | 1e-5);
```

To configure a T1 CRC error major alarm for five errors in 10^{-4} bits, include the `crc-major-alarm-threshold` statement at the `[edit interfaces interface-name t1-options]` hierarchy level, specifying the `5e-4` option:

```
[edit interfaces interface-name t1-options]
  crc-major-alarm-threshold 5e-4;
```

All settings except `1e-5` use a 10-second monitoring period. The `1e-5` value uses a 50-second monitoring period.

