

Configuring Channelized E1 IQ and IQE Interfaces

This section describes how to configure channelized E1 IQ and IQE interfaces, discussing the following topics:

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NOTE: Class-of-service (CoS) rules cannot be applied to an individual channel configured on channelized IQ and IQE interfaces. You can only apply CoS rules to the aggregate bit streams.

Configuring E1 IQ and IQE Interfaces

To configure an E1 interface, include the **no-partition** and **interface-type** statements at the [edit interfaces *ce1-fpc/pic/port*] hierarchy level:

```
[edit interfaces ce1-fpc/pic/port]  
[Unresolved xref] interface-type e1;
```

This configuration creates interface *e1-fpc/pic/port*.

Configuring Fractional E1 IQ and IQE Interfaces

By default, all the time slots on a channelized E1 interface are used. To configure a fractional E1 interface on a Channelized E1 IQ PIC, perform the following tasks:

1. Include the **no-partition** statement at the [edit interfaces *ce1-fpc/pic/port*] hierarchy level:

```
[edit interfaces ce1-fpc/pic/port]  
[Unresolved xref] interface-type e1;
```

This configuration creates interface *e1-fpc/pic/port*.

2. Configure the number of time slots allocated to the E1 IQ or IQE interface by including the **timeslots** statement at the [edit interfaces *e1-fpc/pic/port* **e1-options**] hierarchy level:

```
[edit interfaces e1-fpc/pic/port e1-options]  
timeslots time-slot-range;
```

NxDS0 time slots configured on either a channelized STM1 IQ or IQE interface or a channelized E1 IQ or IQE interface are numbered from 1 to 31 (0 is reserved), while fractional E1 time slots are numbered from 2 to 32 (1 is reserved).

To configure ranges, use hyphens. To configure discontinuous time slots, use commas. Do not include spaces.

For more information about E1 time slots, see [Configuring Fractional E1 Time Slots](#).

Example: Configuring Fractional E1 IQ and IQE Interfaces

Configure a fractional E1 interface that uses time slots 2 through 10:

```
[edit interfaces ce1-0/0/0]
no-partition interface-type e1;
[edit interfaces e1-0/0/0 e1-options]
timeslots 2-10;
```

For a full configuration example, see the *JUNOS Feature Guide*.

Configuring NxDS0 IQ and IQE Interfaces

By default, all the time slots on a channelized E1 interface are used. To configure an NxDS0 IQ interface on a Channelized E1 IQ or IQE PIC, you must configure the number of time slots allocated to the NxDS0 IQ or IQE interface by including the `partition`, `timeslots`, and `interface-type` statements at the `[edit interfaces ce1-fpc/pic/port]` hierarchy level, specifying the `ds` interface type:

```
[edit interfaces ce1-fpc/pic/port]
partition partition-number timeslots time-slot-range interface-type ds;
```

For channelized E1 IQ and IQE interfaces, the partition number range is from 1 through 31.

For E1 IQ and IQE interfaces (`e1-fpc/pic/port`), the time-slot range is from 2 through 31. For channelized E1 IQ and IQE interfaces (`ce1-fpc/pic/port`), the time-slot range is from 1 through 31. You can designate any combination of time slots. To configure ranges, use hyphens. To configure discontinuous time slots, use commas. Do not include spaces. For more information about E1 time slots, see [Configuring Fractional E1 Time Slots](#).

Example: Configuring an NxDS0 IQ or IQE Interface

Configure an NxDS0 interface that uses time slots 2 through 10. This configuration creates the `ds-0/0/0:1:1` interface.

```
[edit interfaces ce1-0/0/0:1]
partition 1 timeslots 2-10 interface-type ds;
```

For a full configuration example, see the *JUNOS Feature Guide*.