

## fcs

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**Syntax** fcs (16 | 32);

**Hierarchy Level** [edit interface e1-*fpc/pic/port*],  
[edit interface t1-*fpc/pic/port*],  
[edit interfaces *interface-name* ds0-options],  
[edit interfaces *interface-name* e1-options],  
[edit interfaces *interface-name* e3-options],  
[edit interfaces *interface-name* sonet-options],  
[edit interfaces *interface-name* t1-options],  
[edit interfaces *interface-name* t3-options]

**Release Information** Statement introduced before JUNOS Release 7.4.

**Description** For E1/E3, SONET/SDH, and T1/T3 interfaces, configure the frame checksum (FCS) on the interface. The checksum must be the same on both ends of the interface.

On a channelized OC12 interface, the SONET/SDH **fcs** statement is not supported. To configure FCS on each DS3 channel, you must include the **t3-options fcs** statement in the configuration for each channel. For SONET/SDH, the channelized OC12 interface supports DS3 to STS-1 to OC12. For SDH, the channelized OC12 interface supports NxDS3 to NxVC3 to AU3 to STM.



**NOTE:** When configuring E1 interfaces on the 10-port Channelized E1/T1 IQE PIC, **fcs** must be set at the [edit interface e1-*fpc/pic/port*] hierarchy level.

When configuring T1 interfaces on the 10-port Channelized E1/T1 IQE PIC, **fcs** must be set at the [edit interface t1-*fpc/pic/port*] hierarchy level.

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**Options** 16—Use a 16-bit frame checksum on the interface.

32—Use a 32-bit frame checksum on the interface. Using a 32-bit checksum provides more reliable packet verification, but some older equipment might not support 32-bit checksums.

**Default:** 16

**Usage Guidelines** See Configuring the E1 Frame Checksum, Configuring the E3 Frame Checksum, Configuring SONET/SDH Physical Interface Properties, Configuring the T1 Frame Checksum, and Configuring the T3 Frame Checksum.

**Required Privilege Level** interface—To view this statement in the configuration.  
interface-control—To add this statement to the configuration.

