

## cell-bundle-size

---

<b>Syntax</b>	cell-bundle-size <i>cells</i> ;
<b>Hierarchy Level</b>	[edit interfaces <i>at-fpc/pic/port</i> atm-options], [edit interfaces <i>at-fpc/pic/port</i> unit <i>logical-unit-number</i> ], [edit logical-systems <i>logical-system-name</i> interfaces <i>at-fpc/pic/port</i> unit <i>logical-unit-number</i> ]
<b>Release Information</b>	Statement introduced before JUNOS Release 7.4.
<b>Description</b>	For ATM2 IQ interfaces using ATM Layer 2 circuit cell-relay transport mode only, you can configure the maximum number of ATM cells per frame.
<b>Options</b>	<i>cells</i> —Maximum number of cells. <b>Default:</b> 1 cell <b>Range:</b> 1 through 176 cells
<b>Usage Guidelines</b>	See Configuring the Layer 2 Circuit Cell-Relay Cell Maximum.
<b>Required Privilege Level</b>	interface—To view this statement in the configuration. interface-control—To add this statement to the configuration.

