

cbit-parity

Syntax	(cbit-parity no-cbit-parity);
Hierarchy Level	[edit interfaces <i>interface-name</i> t3-options]
Release Information	Statement introduced before JUNOS Release 7.4.
Description	For T3 interfaces only, enable or disable C-bit parity mode, which controls the type of framing that is present on the transmitted T3 signal. When C-bit parity mode is enabled, the C-bit positions are used for the far-end block error (FEBE), far-end alarm and control (FEAC), terminal data link, path parity, and mode indicator bits, as defined in ANSI T1.107a-1989. For ATM and ATM2 IQ2 and IQ2-E interfaces, M23 framing is used when the no-cbit-parity statement is included. For all other interfaces, M13 framing is used when the no-cbit-parity statement is included.
Default	C-bit parity mode is enabled.
Usage Guidelines	See Configuring E3 and T3 Parameters on ATM Interfaces and Disabling T3 C-Bit Parity Mode.
Required Privilege Level	interface—To view this statement in the configuration. interface-control—To add this statement to the configuration.

