


bert-error-rate

Syntax	bert-error-rate <i>rate</i> ;
Hierarchy Level	[edit interface <i>ce1-fpc/pic/port</i>] [edit interface <i>ct1-fpc/pic/port</i>] [edit interfaces <i>interface-name</i> ds0-options], [edit interfaces <i>interface-name</i> e1-options], [edit interfaces <i>interface-name</i> e3-options], [edit interfaces <i>interface-name</i> t1-options], [edit interfaces <i>interface-name</i> t3-options]
Release Information	Statement introduced before JUNOS Release 7.4.
Description	Configure the bit error rate to use in a BERT procedure. Applies to E1, E3, T1, or T3 interfaces, and to the channelized interfaces (DS3, OC3, OC12, STM1).
	NOTE: When configuring CE1 interfaces on the 10-port Channelized E1/T1 IQE PIC, bert-error-rate must be set at the [edit interface <i>ce1-fpc/pic/port</i>] hierarchy level. When configuring CT1 interfaces on the 10-port Channelized E1/T1 IQE PIC, bert-error-rate must be set at the [edit interface <i>ct1-fpc/pic/port</i>] hierarchy level.
Options	<i>rate</i> —Bit error rate. Range: 0 through 7, which corresponds to 10^{-1} (1 error per bit) to 10^{-7} (1 error per 10 million bits) Default: 0
Usage Guidelines	See Interface Diagnostics, Configuring E3 BERT Properties, Configuring T1 BERT Properties, Configuring T3 BERT Properties, and [Unresolved xref] .
Required Privilege Level	interface—To view this statement in the configuration. interface-control—To add this statement to the configuration.
Related Topics	bert-algorithm, bert-period

