



CTP System

CTPOS Release Notes

Release 4.4

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CTPOS Release 4.4

Release Overview

These *Release Notes* accompany Release 4.4 of the CTPOS Circuit Emulation software. They describe the enhancements as well as known issues with the software. CTPOS runs on all Juniper Networks CTP 1000 and 2000 series platforms.

If the information in these *Release Notes* differs from the information found in the published documentation set, follow these *Release Notes*. These *Release Notes* include the following sections:

- Release Overview on page 1
- Required Files on page 1
- New Features on page 2
- Enhancements on page 2
- Known Issues on page 3

You can also find these release notes on the Juniper Networks Technical Publications CTP documentation Web page, which is located at <http://www.juniper.net/techpubs/hardware/ctp/>.

Required Files

The following files are required for Release 4.4:

- Full FLASH Image File: flash_4.4R1.img
- Upgrade Archive File: acorn_429_070824.tgz
- CTP2000 FPGA Archive File: acorn_429_070717_fpga_2000_v6d.tgz

New Features

This section describes features new to Release 4.4.

Fractional T1/E1 Support

Fractional T1/E1 transports the first n DS0 channels across the IP network, where n is configurable. There are slight differences depending on whether the fractional support is for T1 or E1. Fractional T1 supports only ESF framing. CRC generation/checking is not supported in either.

For fractional T1, this implementation can also transport the framing bits across the IP network in addition to the DS0s. Transport of the framing bits is configurable, and the default configuration transports the framing.

For ESF framing, the frame synchronization, data link, and CRC framing bits are passed across the IP network untouched.



NOTE: When the framing bits are not transported across the IP network, the regenerated T1 stream at the far end most likely will not have the same data alignment within the nonidle DS0s. In addition, the CRC and data link framing bits are set to 0 in the regenerated T1 stream.

For fractional E1, the framing is contained in the first DS0. This DS0 is always transported unaltered in this implementation. In addition, CAS support is optional. When CAS support is enabled, the sixteenth DS0 is transported.

Security Enhancements

CTPOS Release 4.4 includes more CTPView and CTPOS security architecture including: access management, logging, and monitoring. Additional chapters have been added to the *CTP System Hardware, Installation, and Software Configuration Guide* that discuss the installation, configuration, and use of the CTPView Server Appliance.

Enhancements

This section describes product enhancements available in Release 4.4.

- Numerous security enhancements have been added.
- Issues associated with CDI, Autobaud, and Transparent modes have been addressed.
- Issues associated with DCARD PLD loading have been addressed.

Known Issues

This section identifies known issues in this release.

- CTP 1002 only—A 40 % possibility of system instability exists when you use a CTP 1002 in conjunction with CTPOS Releases 4.3 through 4.4. We recommend that all CTP 1002 users in a production environment use CTPOS release 4.2.

Because the issue can be addressed only at the kernel level, a future release will address this instability. Please contact Juniper Networks Technical Support if you have any questions about this issue.

- CTP 2000 series only—When you upgrade to CTPOS Release 4.4 using the CTPView server appliance, the FPGA of each interface card is upgraded from v69 or v6c to v6d. Although downgrading the software is currently not supported for CTPOS, it is possible to either reburn the removable flash card to an earlier version of CTPOS or replace the current flash card with one containing an older version of code. Once this is done, an FPGA version-matching archive is created and the appropriate FPGA image for the installed CTPOS code can be loaded.

Upgrade instructions to perform this FPGA load using CTPView Server are in file `acorn_fpga_070824.tgz`. For this upgrade to work properly, the support files for v69, v6c, and v6d of the FPGA image must also reside on the CTPView Server. These files can be found at <http://www.juniper.net/techpubs/hardware/ctp/>.

If you have any questions regarding this procedure, please contact Juniper Networks Technical Support.

